High Input Voltage LDO Linear Regulators

GENERAL DESCRIPTION

The PW6206 series is a high accuracy, high input voltage low quiescent current, high speed, and low dropout liner regulator with high ripple rejection. The input voltage is up to 40V and load current is up to 300mA at VOUT = 5V & VIN = 7V. The device is manufactured with BCD process. The PW6206 offers over-current limit, soft start and over temperature protection to ensure the device working in well conditions

The PW6206 regulators is available in standard SOT89-3L,and SOT23-3L packages. Standard products are Pb-free and Halogen-free.

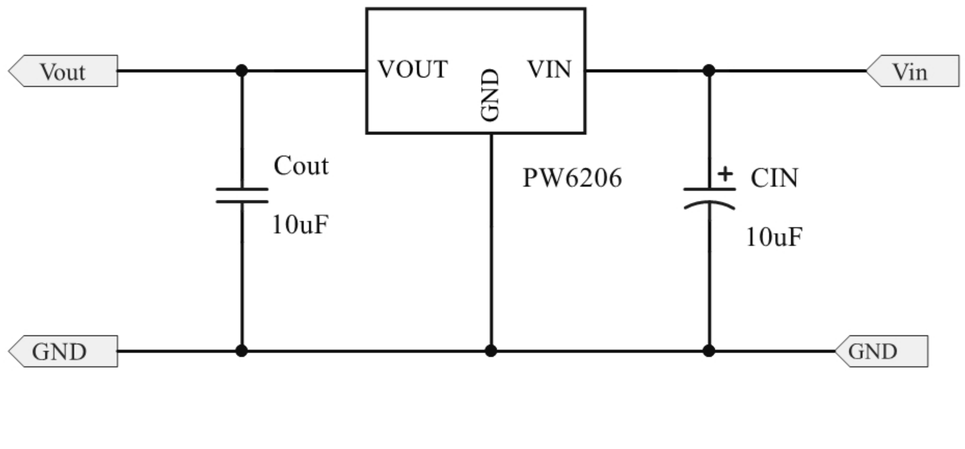
FEATURES

- Input voltage: 4.75V~40V
- Output voltage: 1.8V~5.7V
- Output accuracy: < ±2%
- Output current: 150mA (Typ.)
- Up to 300mA @ VIN = 7V, VOUT = 5V, PW6206B50HV package
- PSRR: 60dB @ 100Hz
- Dropout voltage: 600mV @ IOUT = 100mA
- Quiescent current: 4.2μA @ VIN = 12V(Typ.)
- ESD HBM: 8KV
- Recommend capacitor: 10μF

APPLICATIONS

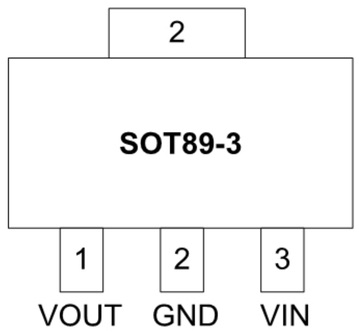
- Smart electric meter
- In-car entertainment
- Electric bicycle

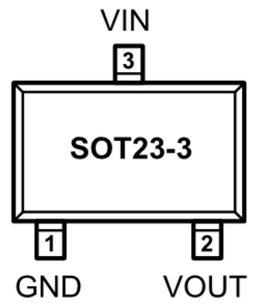
TYPICAL APPLICATION CIRCUIT



PIN ASSIGNMENT/DESCRIPTION

(TOP VIEW)





Product Series	Package	Pin Number	Pin Name	Functions
PW6206AXXHV	SOT23-3	1	GND	Ground
		2	VOUT	Output
		3	VIN	Input
PW6206BXXHV	SOT89-3	1	VOUT	Output
		2	GND	Ground
		3	VIN	Input

XX : Output Voltage. (PW6206A50HV, VOUT:5V
PW6206A33HV, VOUT:3.3V)

Absolute Maximum Ratings (note)

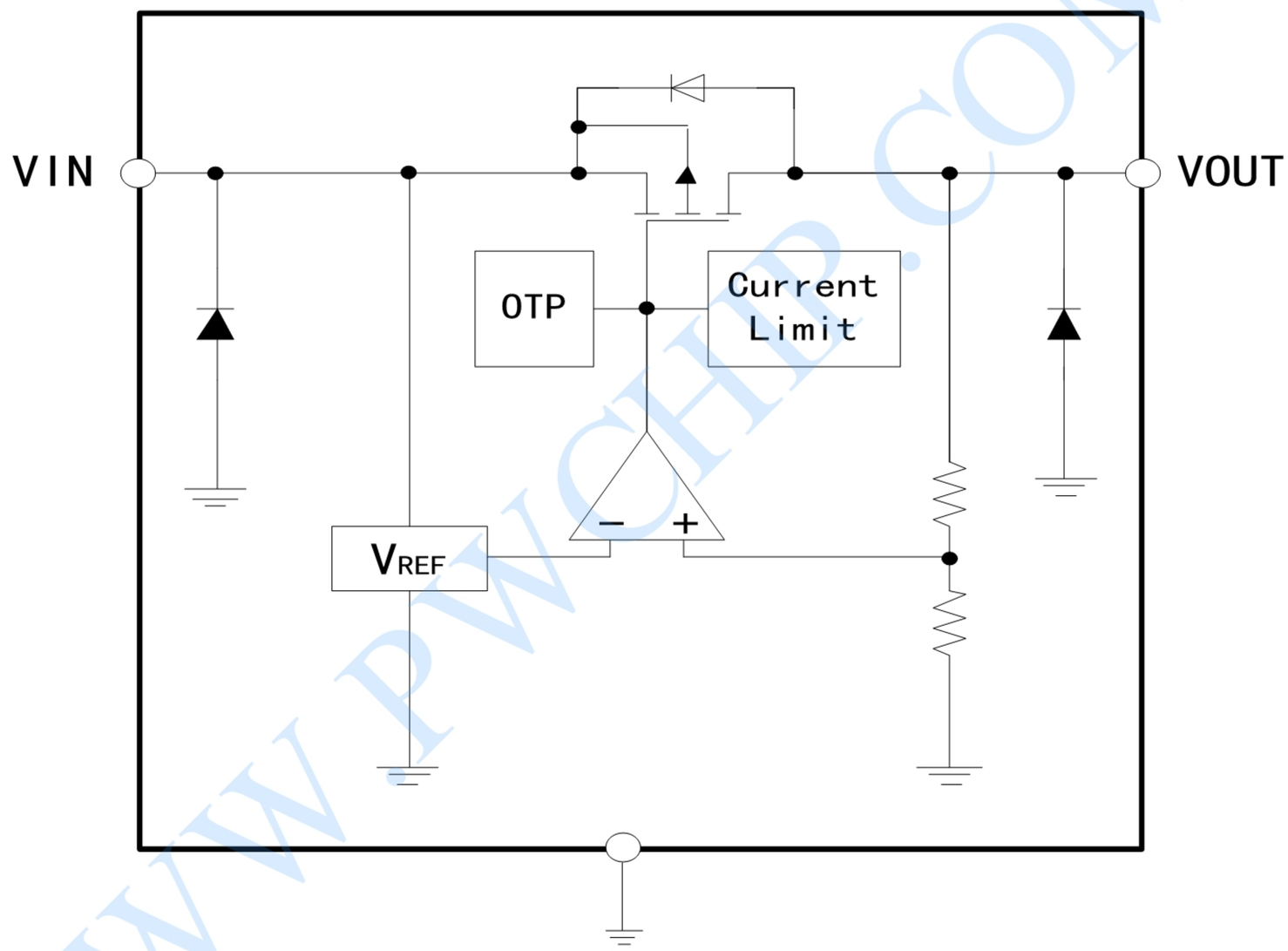
SYMBOL	ITEMS	VALUE	UNIT
VIN	Input Voltage	-0.3~45	V
VOUT	Output Voltage	-0.3~6.5	V
PDMAX	Power Dissipation	OTP limited	W
TJ	Junction Temperature	-40~150	℃
Tstg	Storage Temperature	-55 to 150	℃
Tsolder	Package Lead Soldering Temperature (10s)	260	℃
ESD MM	Machine Mode	200	V
ESD HBM	Human Body Mode	8000	V

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMANDED OPERATING RANGE

SYMBOL	ITEMS	VALUE	UNIT
VIN	VIN Supply Voltage	4.75 to 40	V
RθJA	Thermal Resistance on PCB	45	℃/W
TOPT	Operating Temperature	-40 to +105	℃

SIMPLIFIED BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

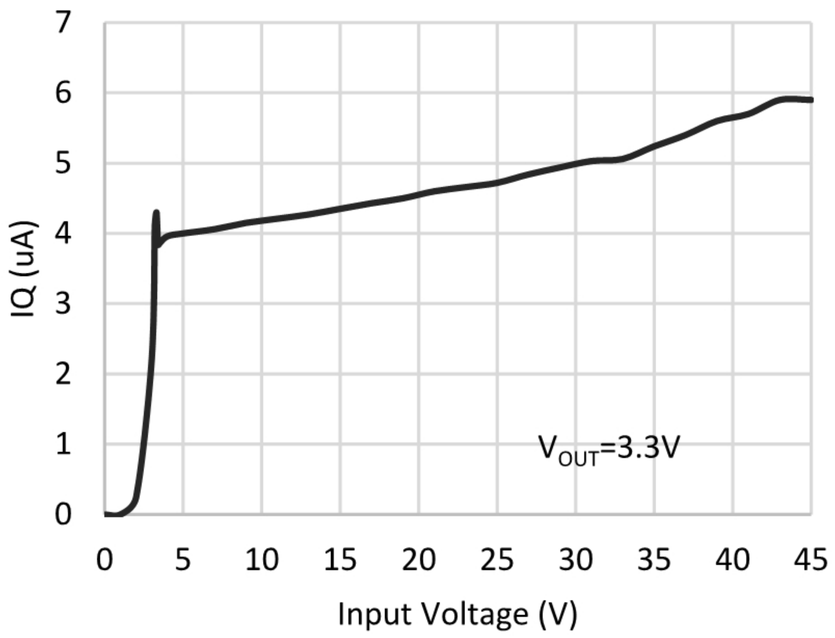
The following specifications apply for VIN = 12V, TA = 25℃, CIN = COUT = 10μF, unless specified otherwise.

SYMBOL	ITEMS	CONDITIONS		MIN	TYP	MAX	UNIT
V_{IN}	Input Range	I_{OUT} = 10mA		4.75		40	V
V_{OUT}	Output Range	I_{OUT} = 10mA		V_{OUT}x0.98	V_{OUT}	V_{OUT}x1.02	V
$\triangle V_{OUT}$	Output Voltage	V_{IN} = 12V, I_{OUT} = 10mA		4.9	5	5.1	V
				3.234	3.3	3.366	
				2.94	3.0	3.06	
I_Q	Quiescent Current	V_{IN} = 7V, I_{OUT} = 0			4	6	μA
		V_{IN} = 24V, I_{OUT} = 0			4.6	6.7	
		V_{IN} = 40V, I_{OUT} = 0			5.4	8.2	
I_{OUT_PK}	Maximum Output Current	V_{IN} - V_{OUT} = 4V, R_L=1Ω			500	550	mA
V_{DROP}	Dropout Voltage	I_{OUT} = 10mA			60	90	mV
		I_{OUT} = 100mA			600	900	
$\triangle V_{LINE}$	Line Regulation	V_{IN} =7 ~ 24V, V_{OUT} = 5V, I_{OUT} = 1mA			0.02	0.03	%/V
		V_{IN} = 7 ~ 45V, V_{OUT} = 5V, I_{OUT} = 1mA			0.08	0.1	
$\triangle V_{LOAD}$	Load Regulation	V_{IN} = 7V, I_{OUT} = 1 ~ 100mA			19	37	mV
I_{SHORT}	Short Current	V_{OUT} Short to GND with 1Ω (1ms pulse), V_{IN} = 12V			180		mA
PSRR	Power Supply Rejection Rate	V_{IN} = 10V, V_{PP} = 0.5V, I_{OUT} = 1mA	F = 100Hz		60		dB
			F = 1kHz		50		
			F = 10kHz		40		
e_{NO}	Output Noise Voltage	10Hz to 100kHz, C_{OUT} = 10μF, I_{OUT}=10mA			±100		μV_{RMS}
T_{SD}	Thermal Shutdown Protection	V_{IN} = 12V, I_{OUT} = 1mA			165		℃
$\Delta V_O/\Delta T$	Temperature Coefficient				±0.5		mV/℃

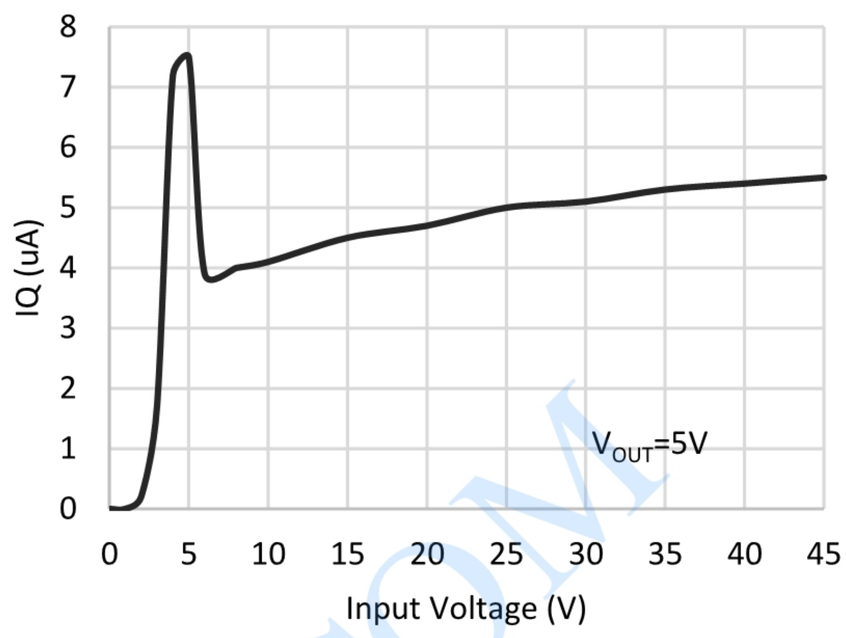
TYPICAL PERFORMANCE CHARACTERISTICS

C_{IN} = 10μF, C_{OUT} = 10μF, T_{OPT} = 25°C, unless specified otherwise. (SOT89-3 Package)

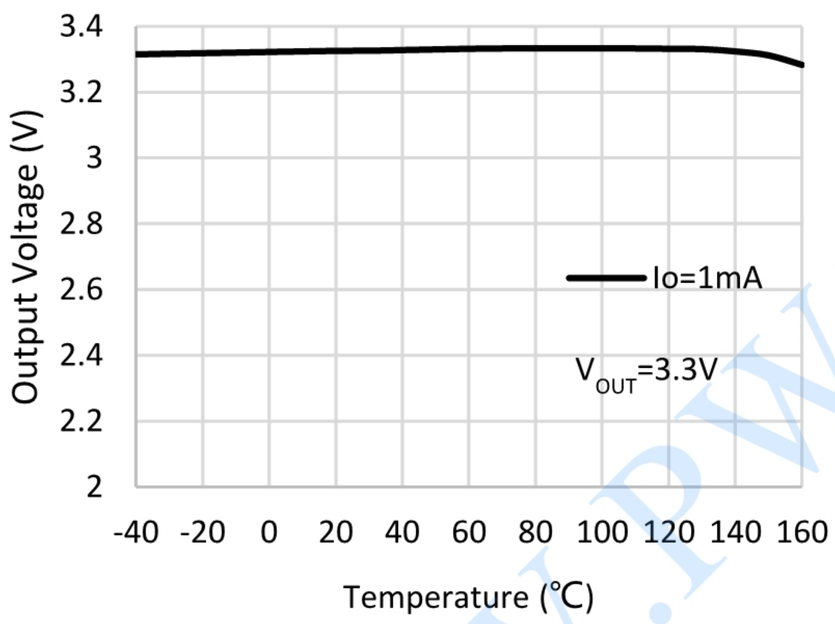
IQ vs. Input Voltage



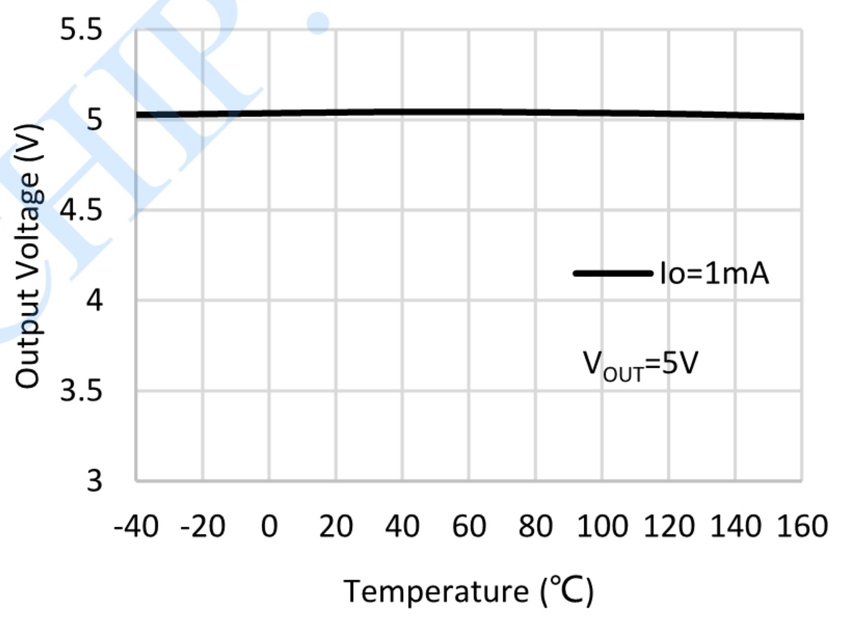
IQ vs. Input Voltage



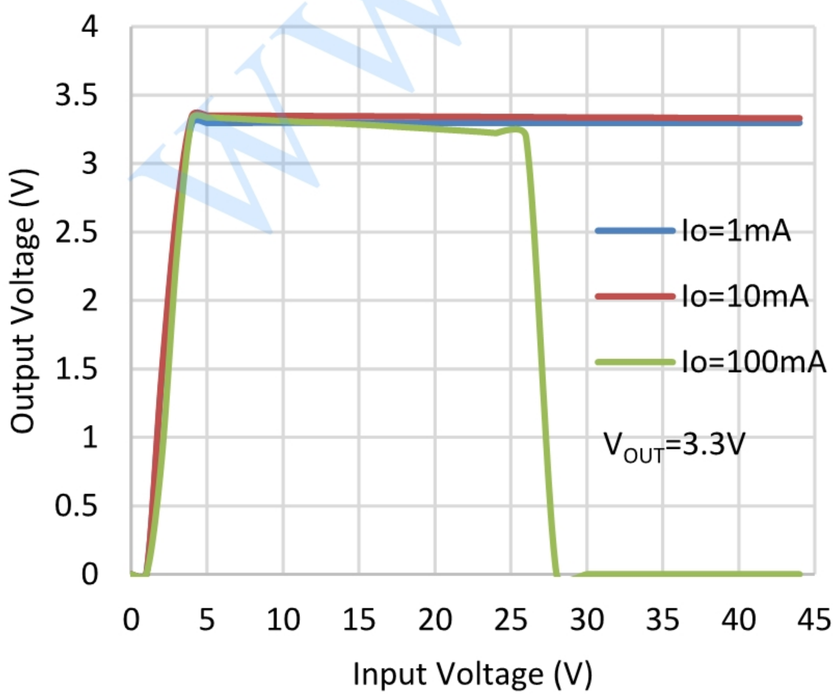
Output Voltage vs. Temperature



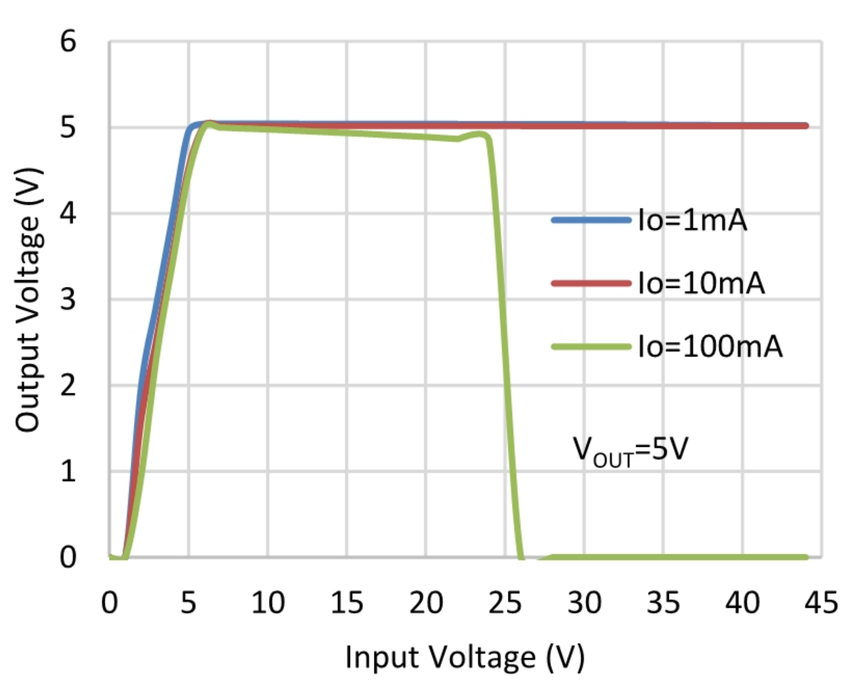
Output Voltage vs. Temperature



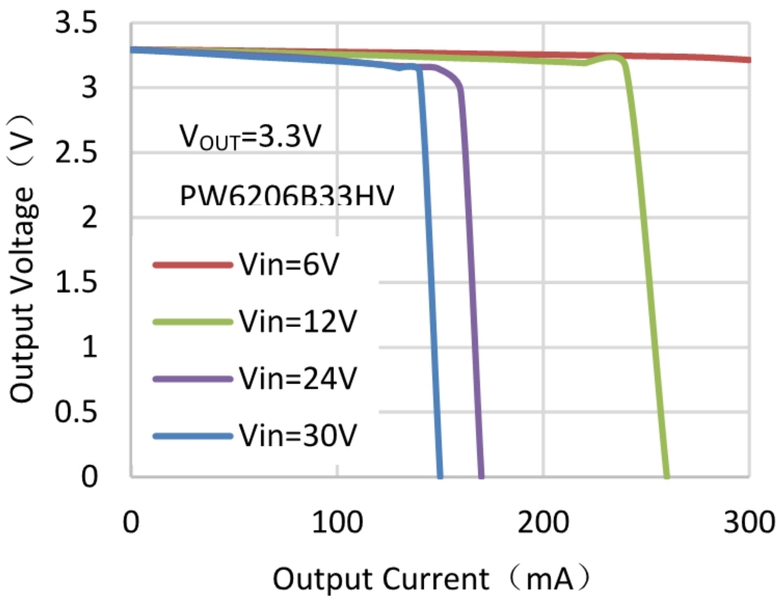
Output Voltage vs. Input Voltage



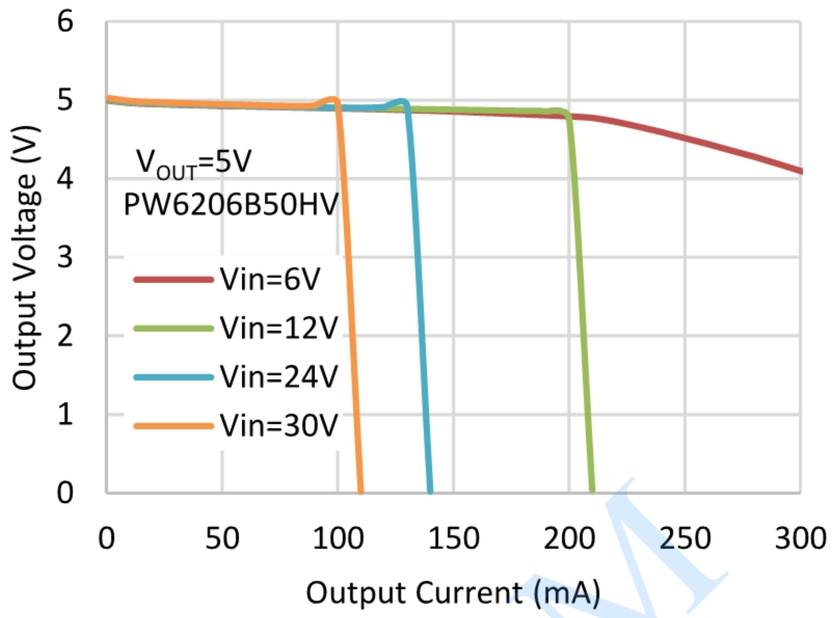
Output Voltage vs. Input Voltage



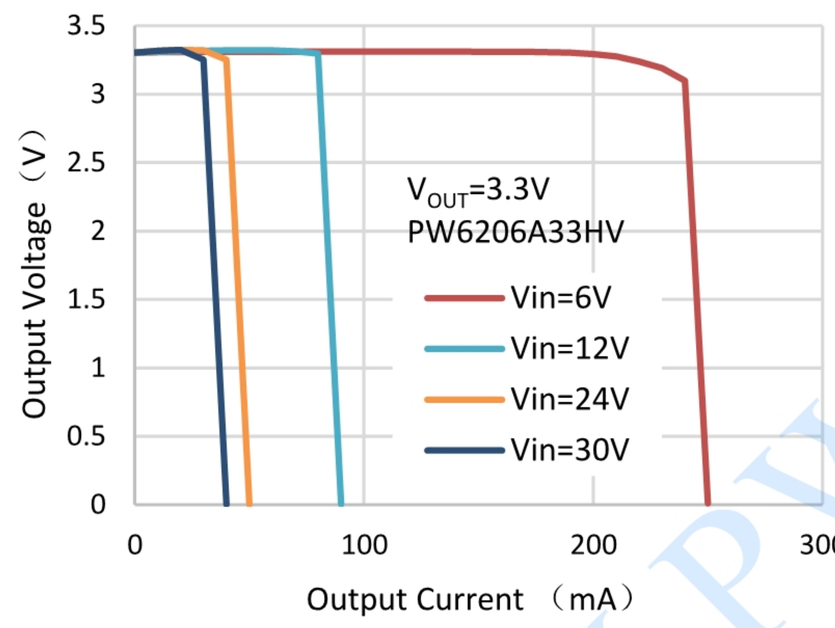
Output Voltage vs. Output Current



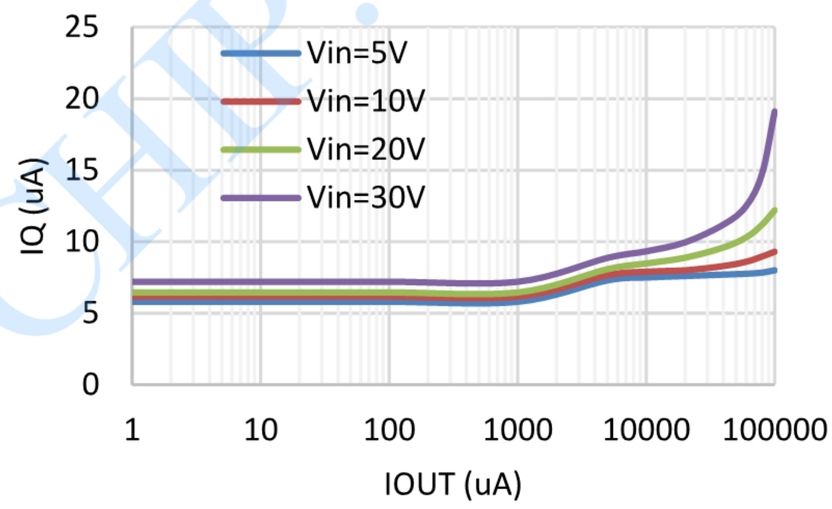
Output Voltage vs. Output Current



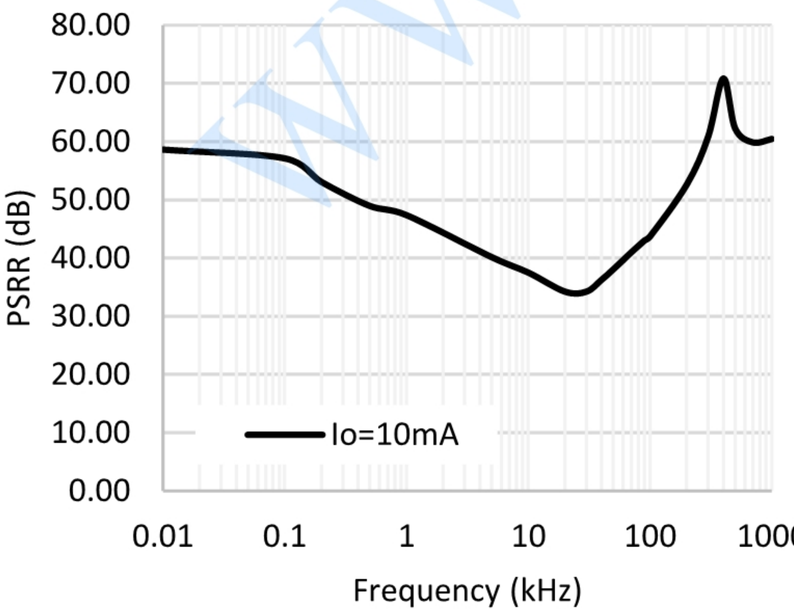
Output Voltage vs. Output Current



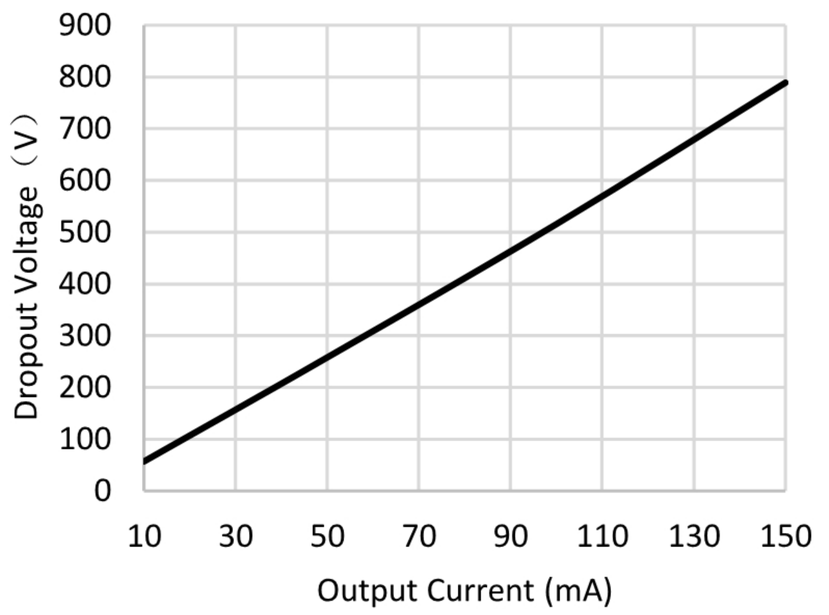
IQ vs. IOUT



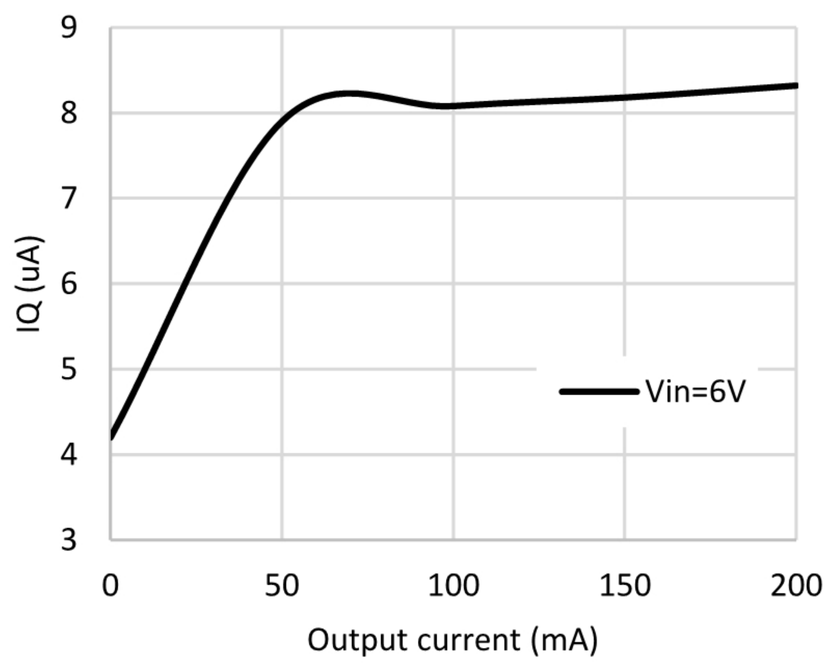
PSRR vs. Frequency



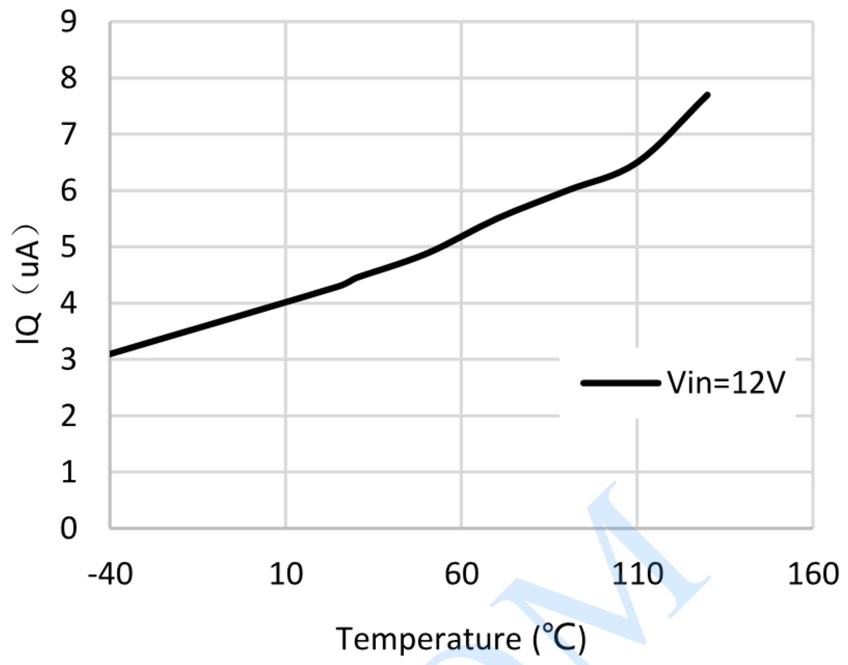
Dropout Voltage vs. Output Current



IQ vs. Output current



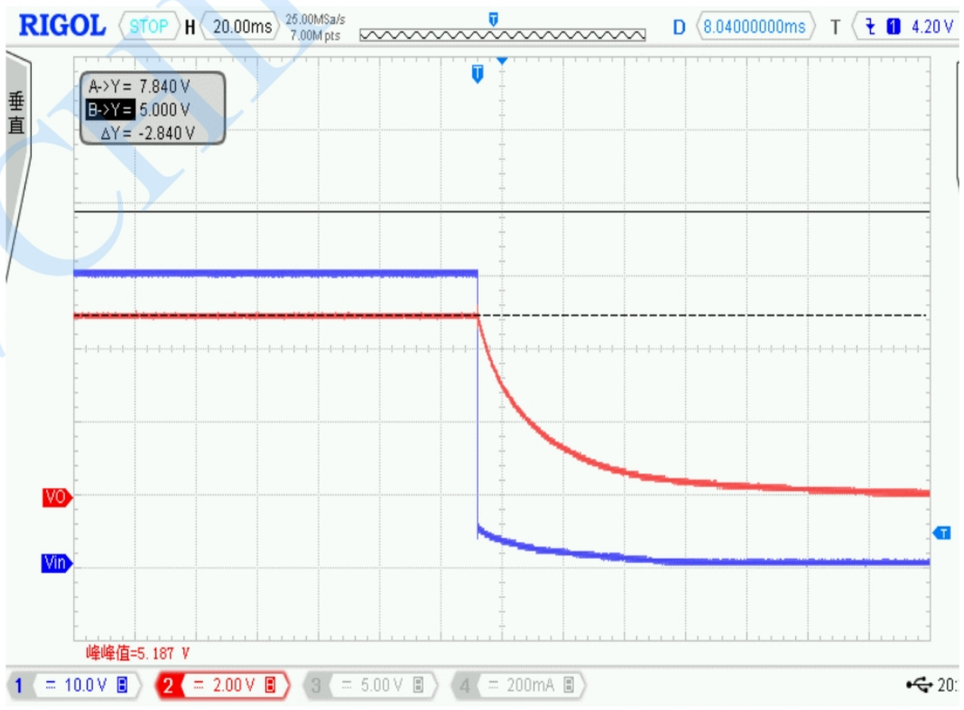
IQ vs. Temperature



Power ON/OFF

CH1： V_{IN}　　CH2： V_{OUT}

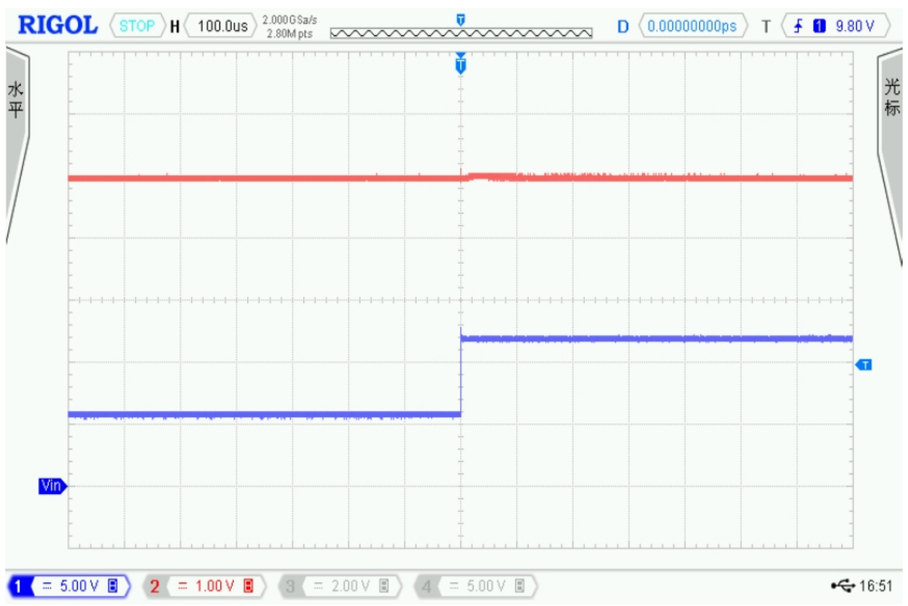
V_{IN}=40V　　I_{OUT}=1mA　　V_{OUT}=5V



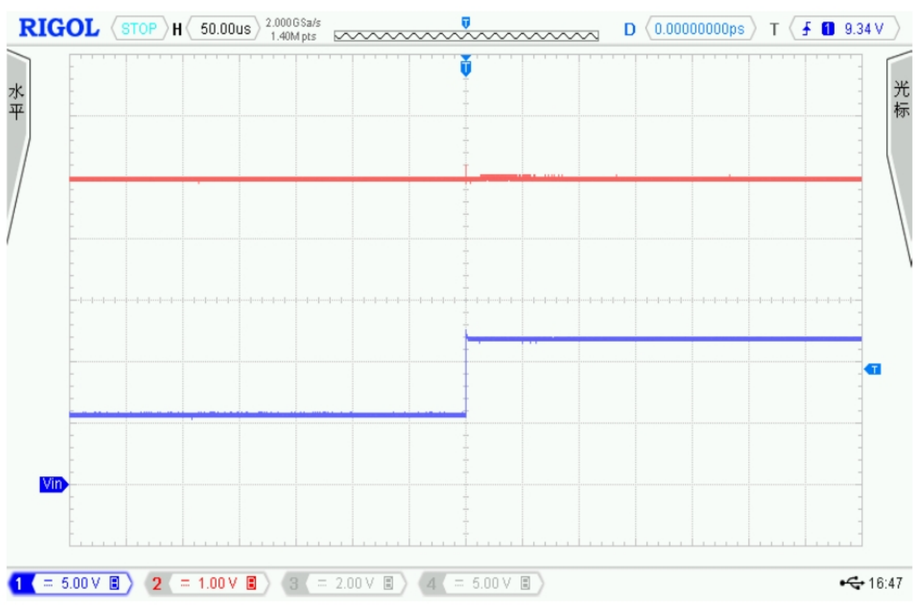
Line Transient

CH1： V_{IN}　　CH2： V_{OUT}

V_{IN}=6V-12V　　I_{OUT}=1mA　　V_{OUT}=5V



V_{IN}=6V-12V　　I_{OUT}=10mA　　V_{OUT}=5V



APPLICATION INFORMATION

INPUT CAPACITOR

An input capacitor of 10μF is required between the VIN and GND pin. The capacitor shall be placed as close as possible to VIN pin, and the use of electrolytic capacitors is recommended. The tolerance and temperature coefficient must be considered in order to ensure the capacitor work within the operation range over the full range of temperature and operating conditions.

OUTPUT CAPACITOR

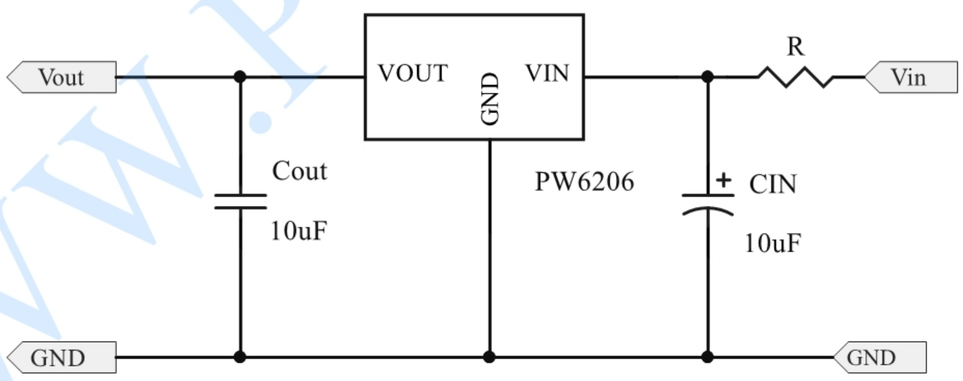
In applications, it is important to select the output capacitor for stable operation. The minimum capacitance for stable and correct operation is 1μF. The capacitance tolerance should be ±30% or better over the operation temperature range. The recommended capacitor type is MLCC.

NO-LOAD STABILITY

The PW6206 will remain stable and in regulation with no external load. This is especially important in CMOS RAM keep-alive applications.

TYPICAL CIRCUIT

The following figure shows a typical application circuit for the PW6206 devices. The value of external components shall be chosen carefully, depending on the application. In plugging application, because the overshoot caused by the insertion and withdrawal of power on the chip may damage the chip, it is recommended that VIN be less than 30V and the input voltage spike should not exceed 45V.

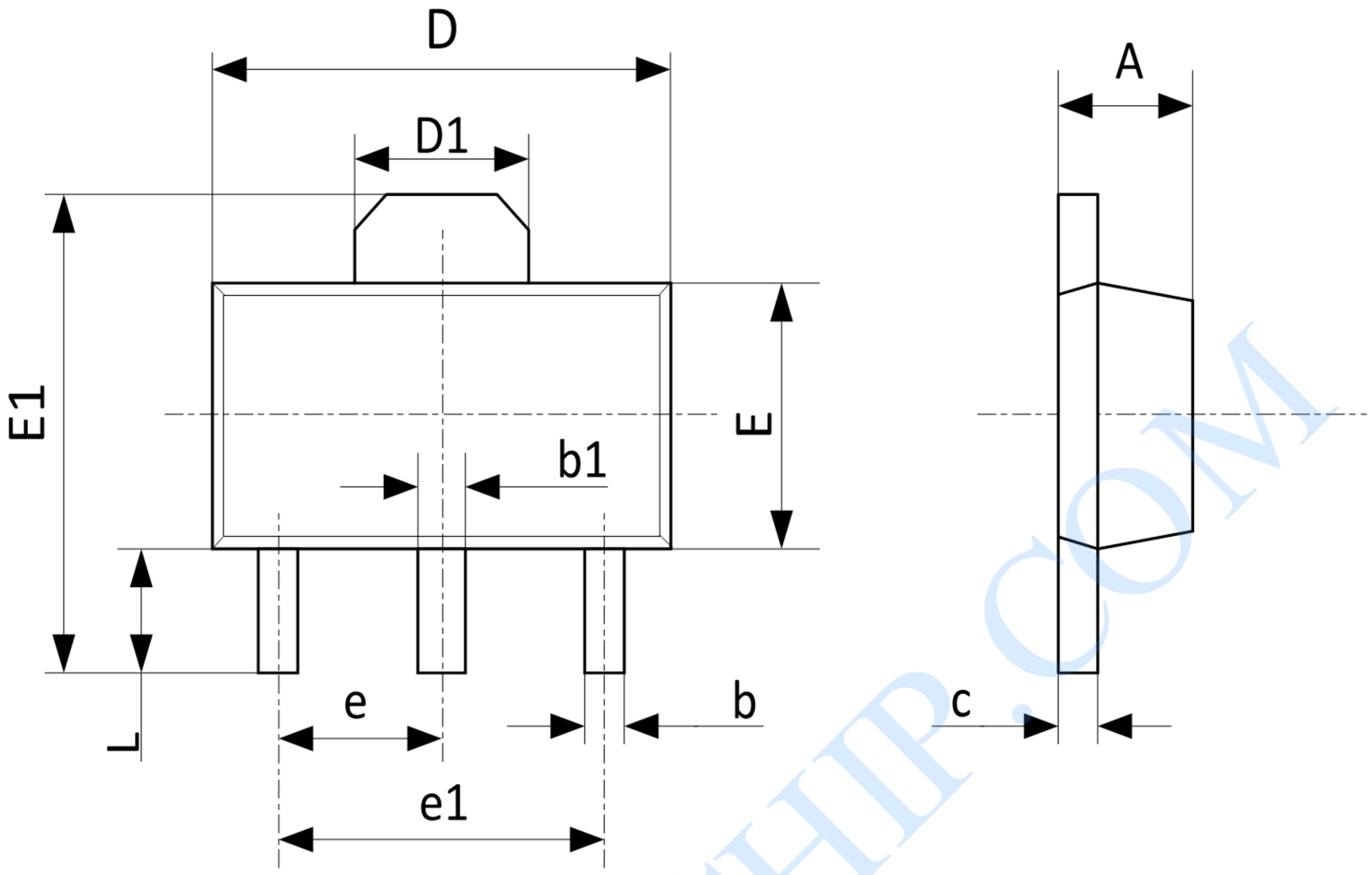


In plugging application, it is suggested that R, Cin are selected as following:

1. Cin=10μF ~ 100μF electrolytic capacitor with maximum voltage greater than 50V, R=0;
2. Cin=1μF ~ 10μF MLCC with maximum voltage Vgreater than 50V and R=2Ω in the type of 1206 - the resistor shall be carefully chosen to make sure enough margin to sustain the surge current during plugging.

PACKAGE DESCRIPTION

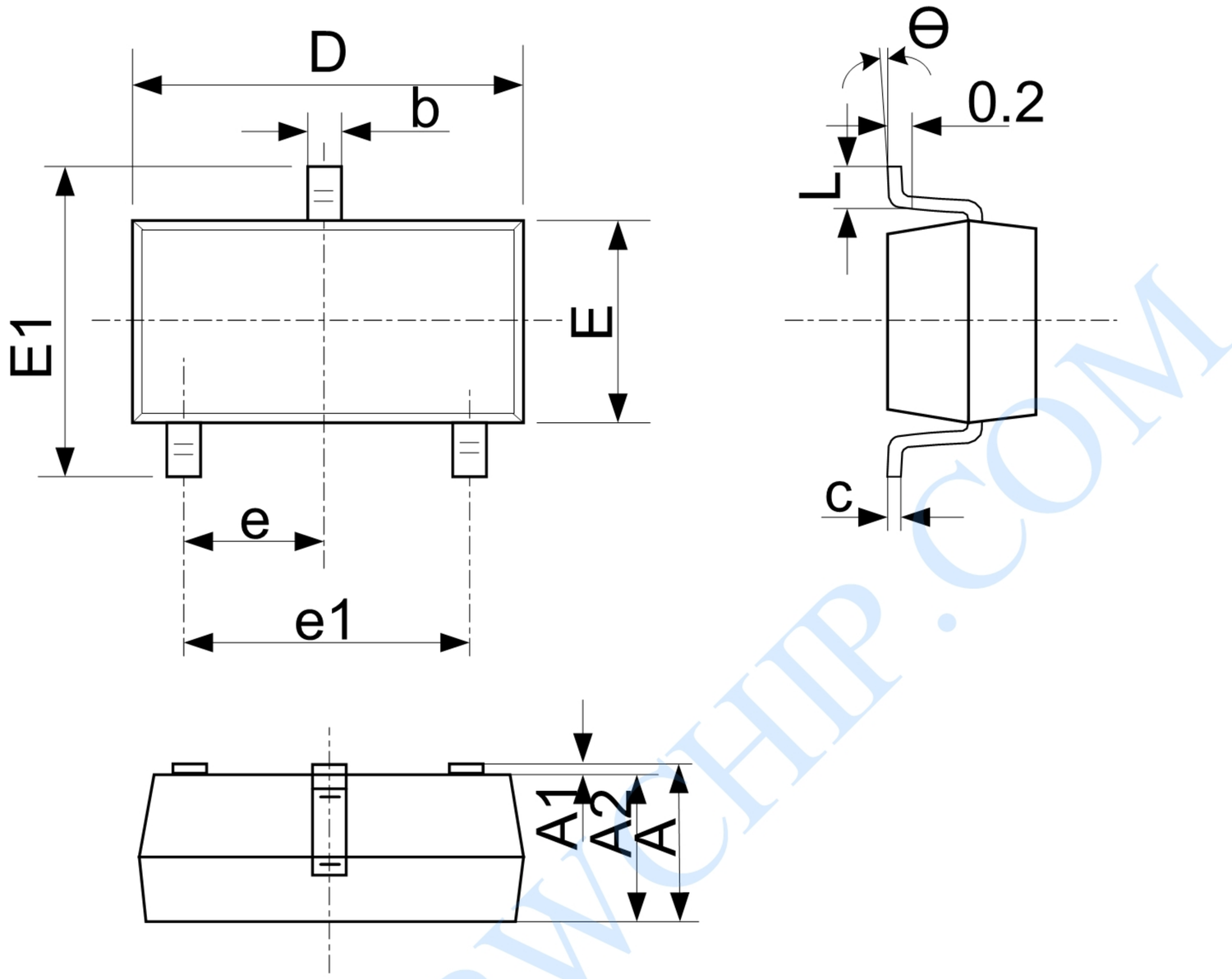
SOT89-3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.45	1.65	0.057	0.065
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060 TYP	
e1	3.000 TYP		0.118 TYP	
L	0.900	1.200	0.035	0.047

PACKAGE DESCRIPTION

SOT23-3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0℃	8℃	0℃	8℃

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