

P-Channel Enhancement Mode MOSFET

GENERAL DESCRIPTION

The PW2307 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application

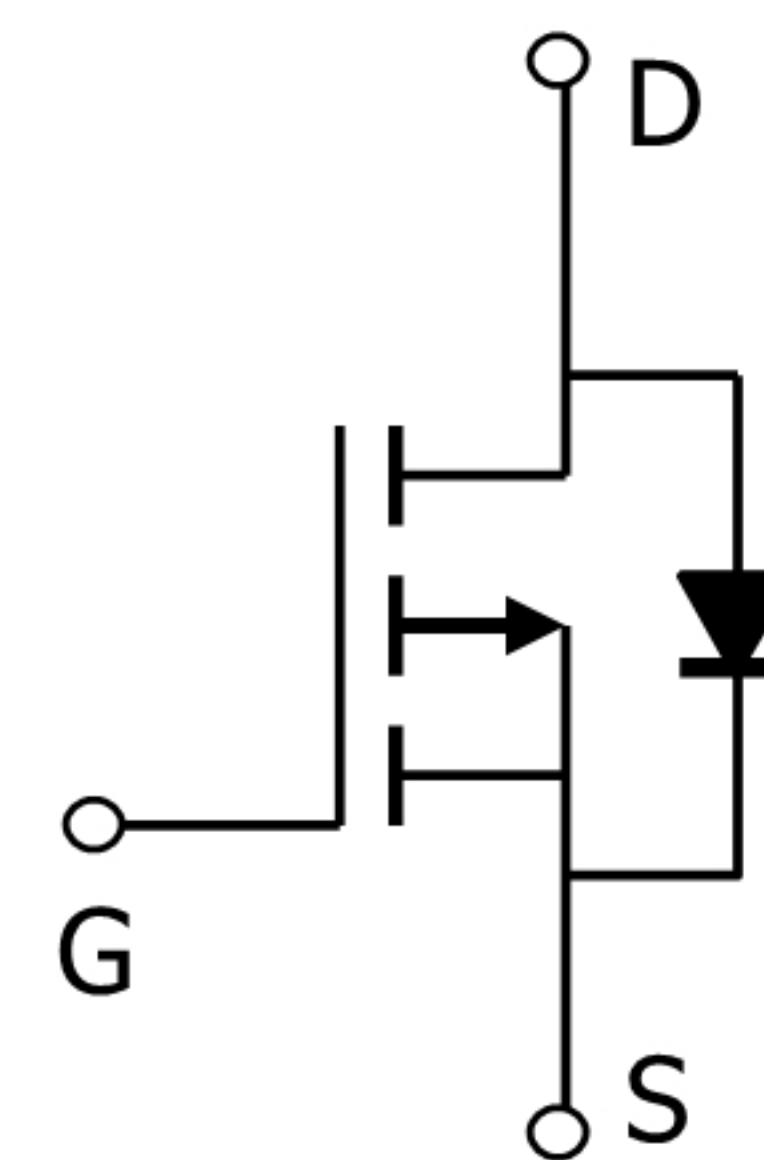
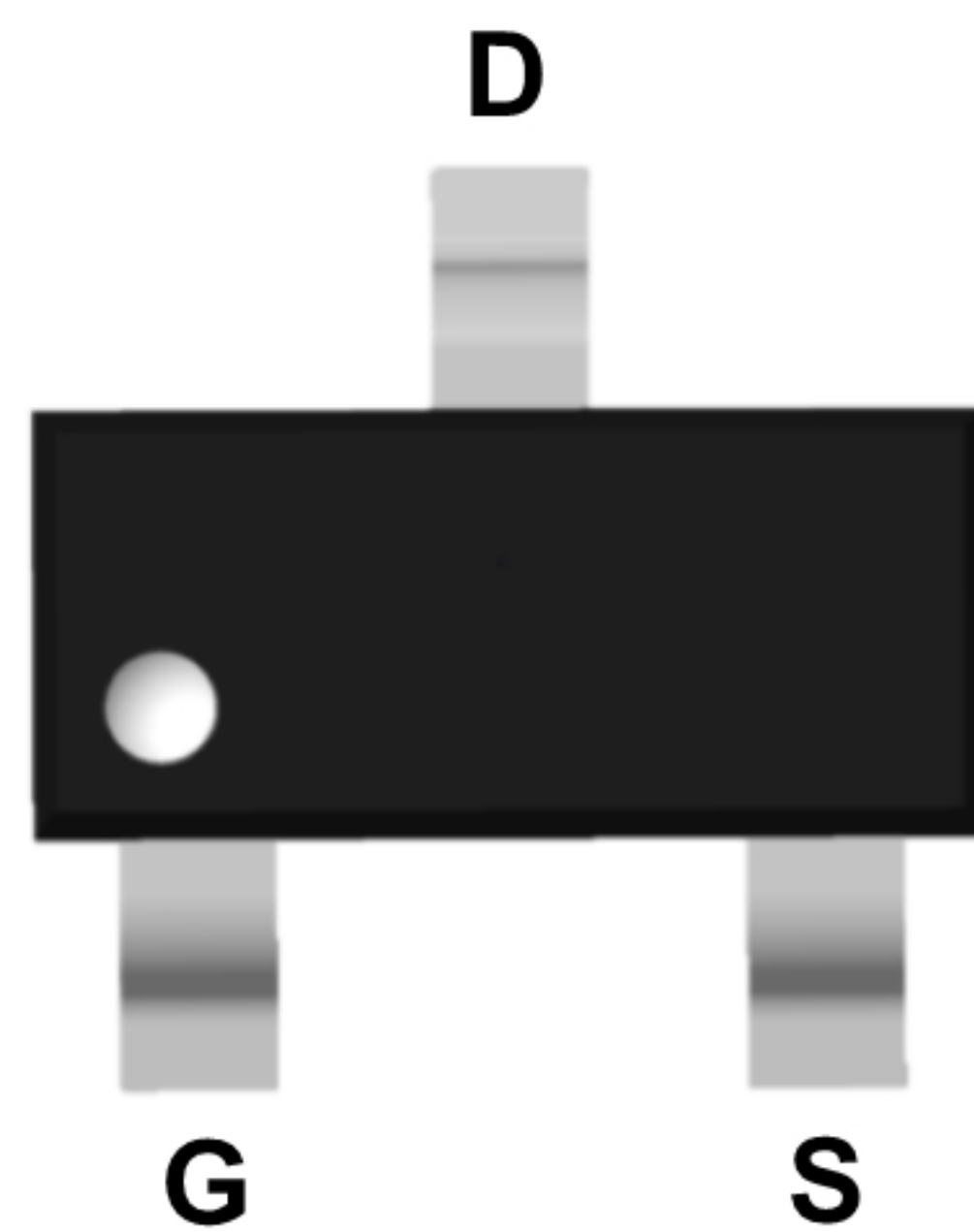
FEATURES

$V_{DS} = -20V$ $I_D = -7A$

$R_{DS(ON)} < 20m\Omega$ @ $V_{GS} = -4.5V$

Available in a 3-Pin SOT23-3 Package

SOT-23-3L
(TOP VIEW)



Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_{D@T_A=25^\circ C}$	Continuous Drain Current, V_{GS} @ -4.5V ¹	-7.1	A
$I_{D@T_A=70^\circ C}$	Continuous Drain Current, V_{GS} @ -4.5V ¹	-4.8	A
I_{DM}	Pulsed Drain Current ²	-23.8	A
$P_{D@T_A=25^\circ C}$	Total Power Dissipation ³	1	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	125	$^\circ C/W$
$R_{\theta JAC}$	Thermal Resistance Junction-Case ¹ ($t \leq 10s$)	80	$^\circ C/W$

ELECTRICAL CHARACTERISTICS

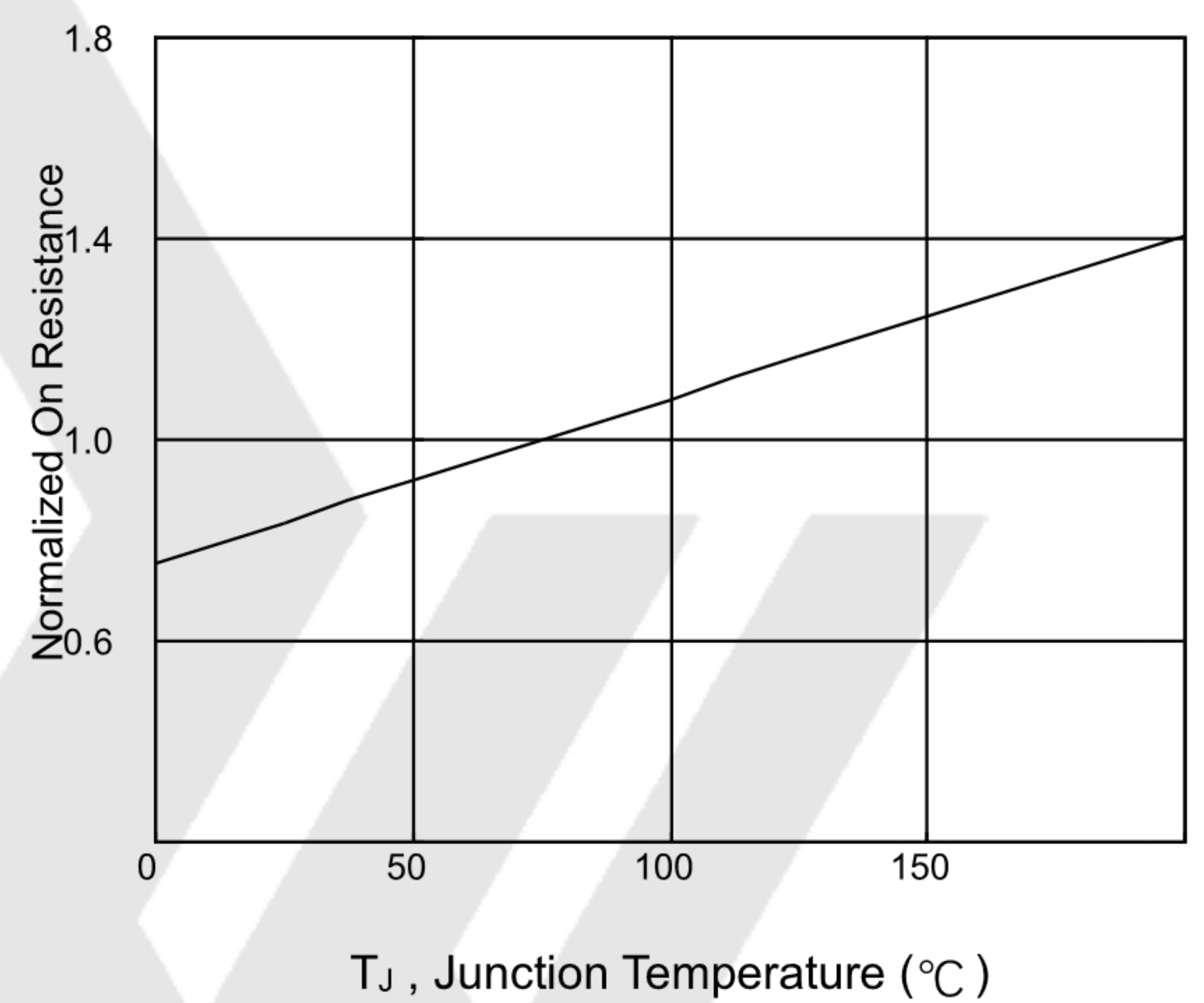
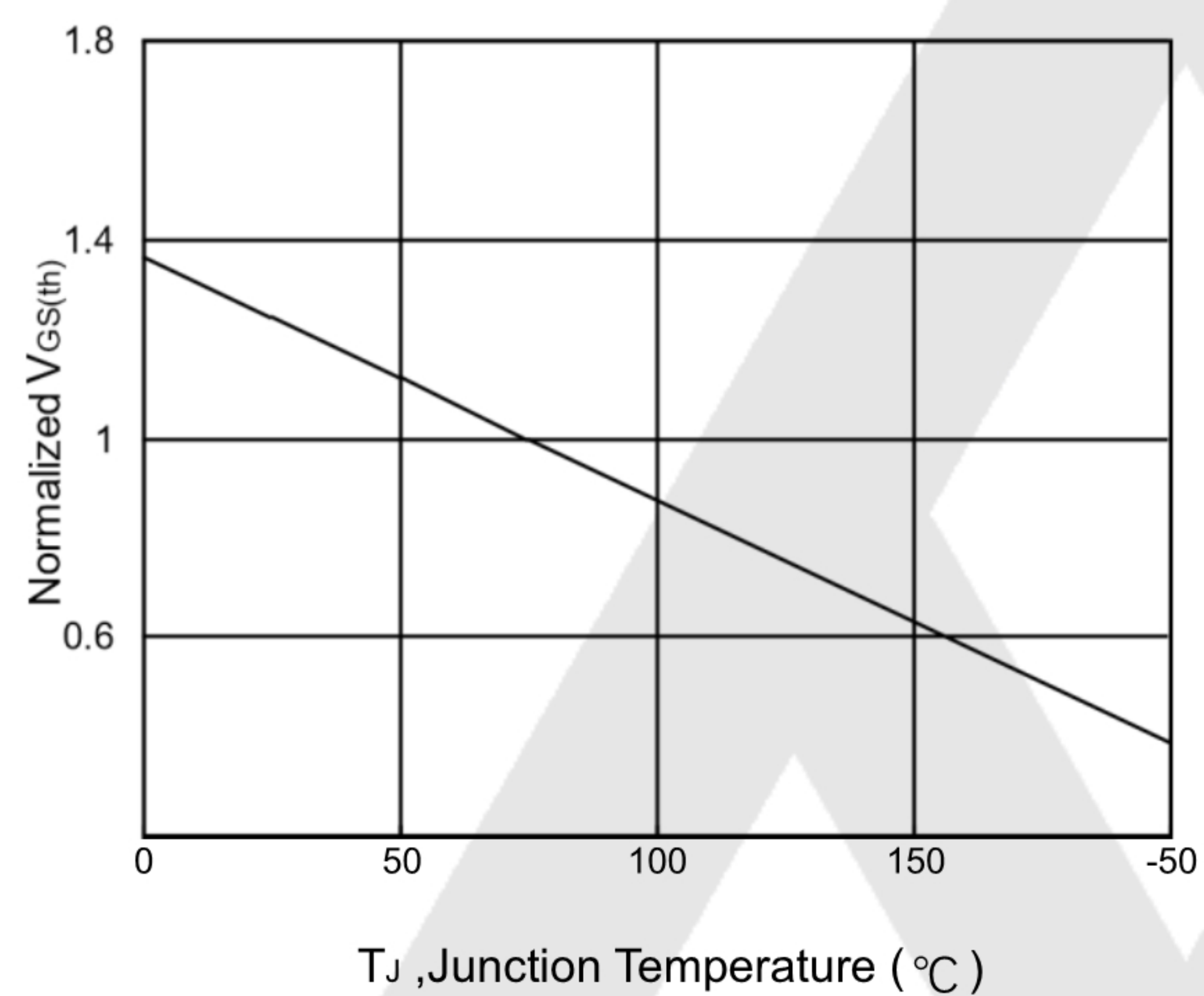
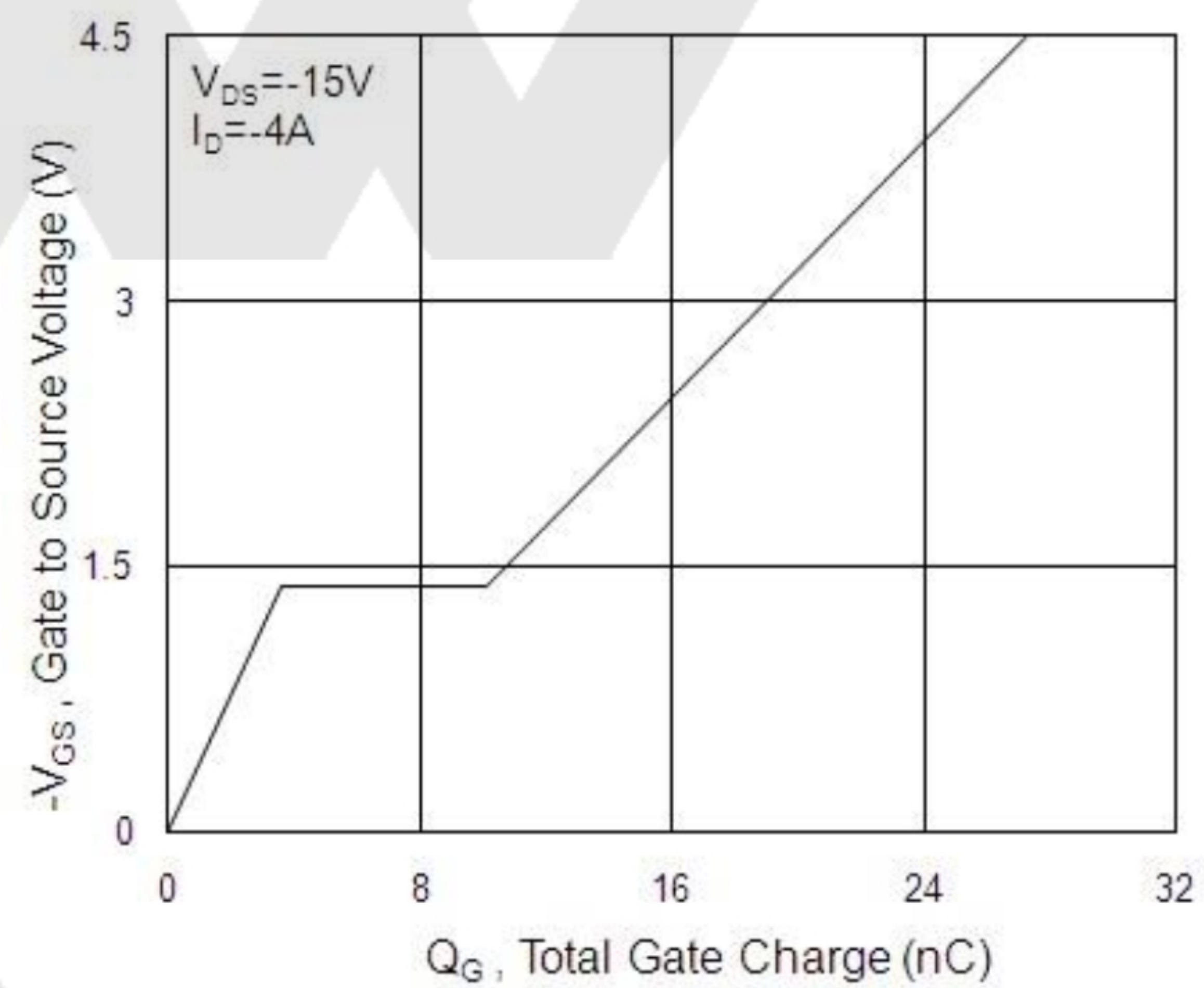
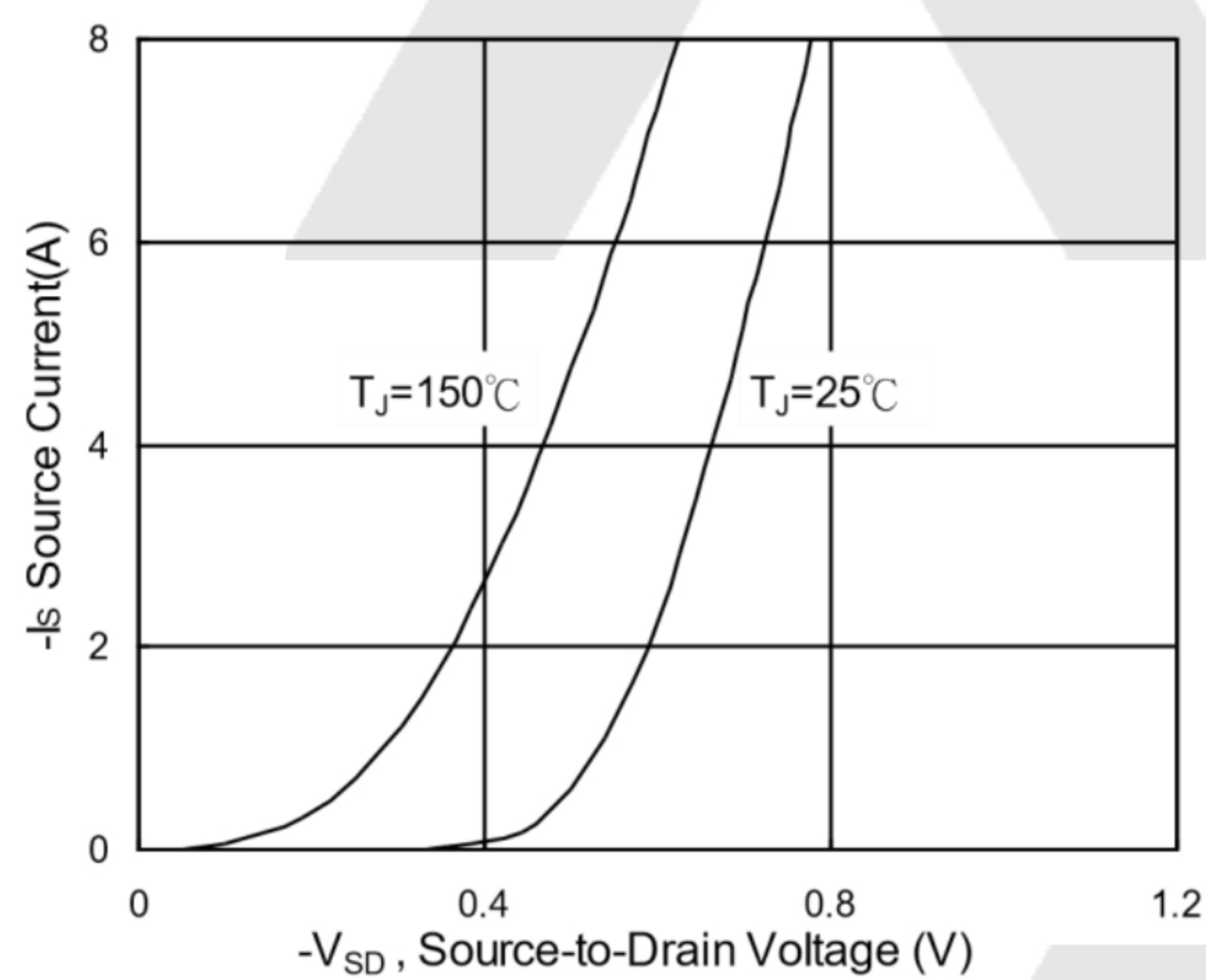
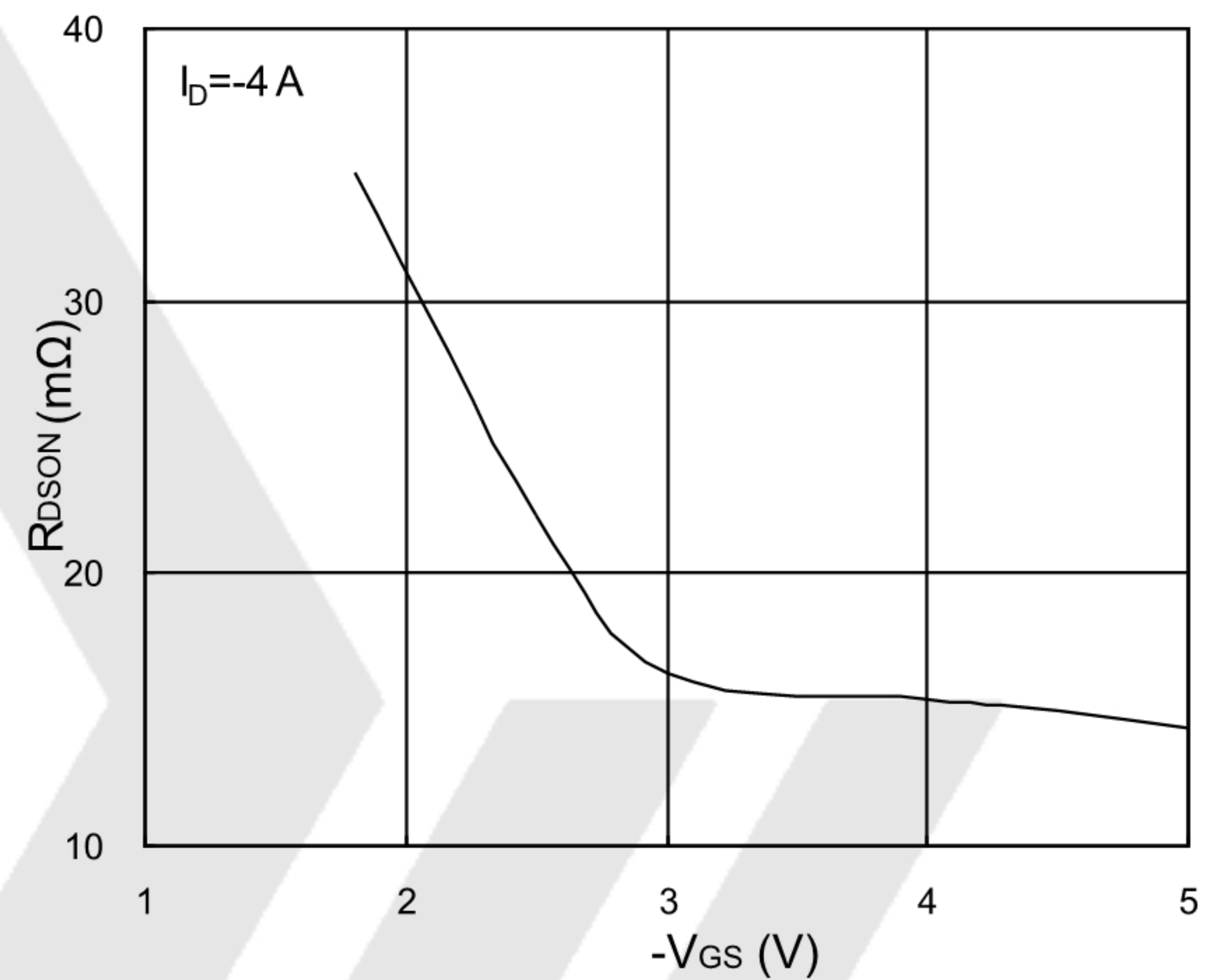
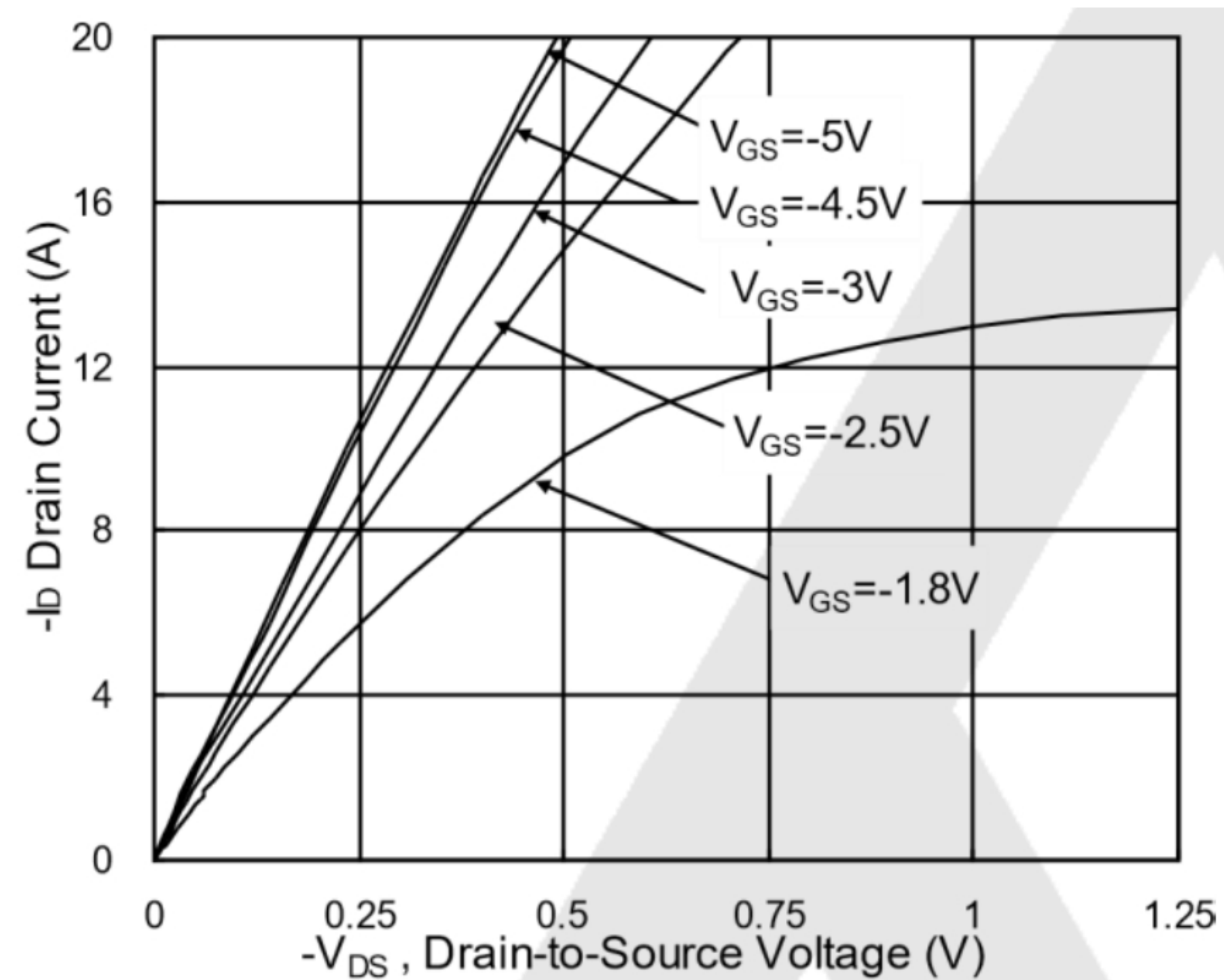
(TA = 25°C, unless otherwise noted.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-20	-22	---	V
ΔBVDSS/ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C , I _D =-1mA	---	-0.01	---	V/°C
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V , I _D =-4A	---	16	21	mΩ
		V _{GS} =-2.5V , I _D =-3A	---	20	28	
		V _{GS} =-1.8V , I _D =-1.5A		28	35	
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.4	-0.7	-1.0	V
ΔVGS(th)	VGS(th) Temperature Coefficient		---	2.96	---	mV/°C
IDSS	Drain-Source Leakage Current	V _{DS} =-16V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-16V , V _{GS} =0V , T _J =55°C	---	---	-5	
IGSS	Gate-Source Leakage Current	V _{GS} =±12V , V _{DS} =0V	---	---	±100	nA
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-4A	---	21	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-4A	---	27.3	38.2	nC
Q _{gs}	Gate-Source Charge		---	3.6	5.0	
Q _{gd}	Gate-Drain Charge		---	6.5	9.1	
Td(on)	Turn-On Delay Time	V _{DD} =-10V, V _{GS} =-4.5V , R _G =3.3Ω I _D =-4A	---	9.2	18.4	ns
T _r	Rise Time		---	59	106	
Td(off)	Turn-Off Delay Time		---	99	198	
T _f	Fall Time		---	71	142	
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	2280	3192	pF
C _{oss}	Output Capacitance		---	220	308	
C _{rss}	Reverse Transfer Capacitance		---	187	262	
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V , Force Current	---	---	-4.7	A
I _{SM}	Pulsed Source Current ^{2,4}		---	---	-18.8	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1	V
t _{rr}	Reverse Recovery Time	I _F =-4A , di/dt=100A/μs , T _J =25°C	---	52	---	nS
Q _{rr}	Reverse Recovery Charge		---	28	---	nC

Note :

- 1、 The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、 The power dissipation is limited by 150°C junction temperature
- 4、 The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics



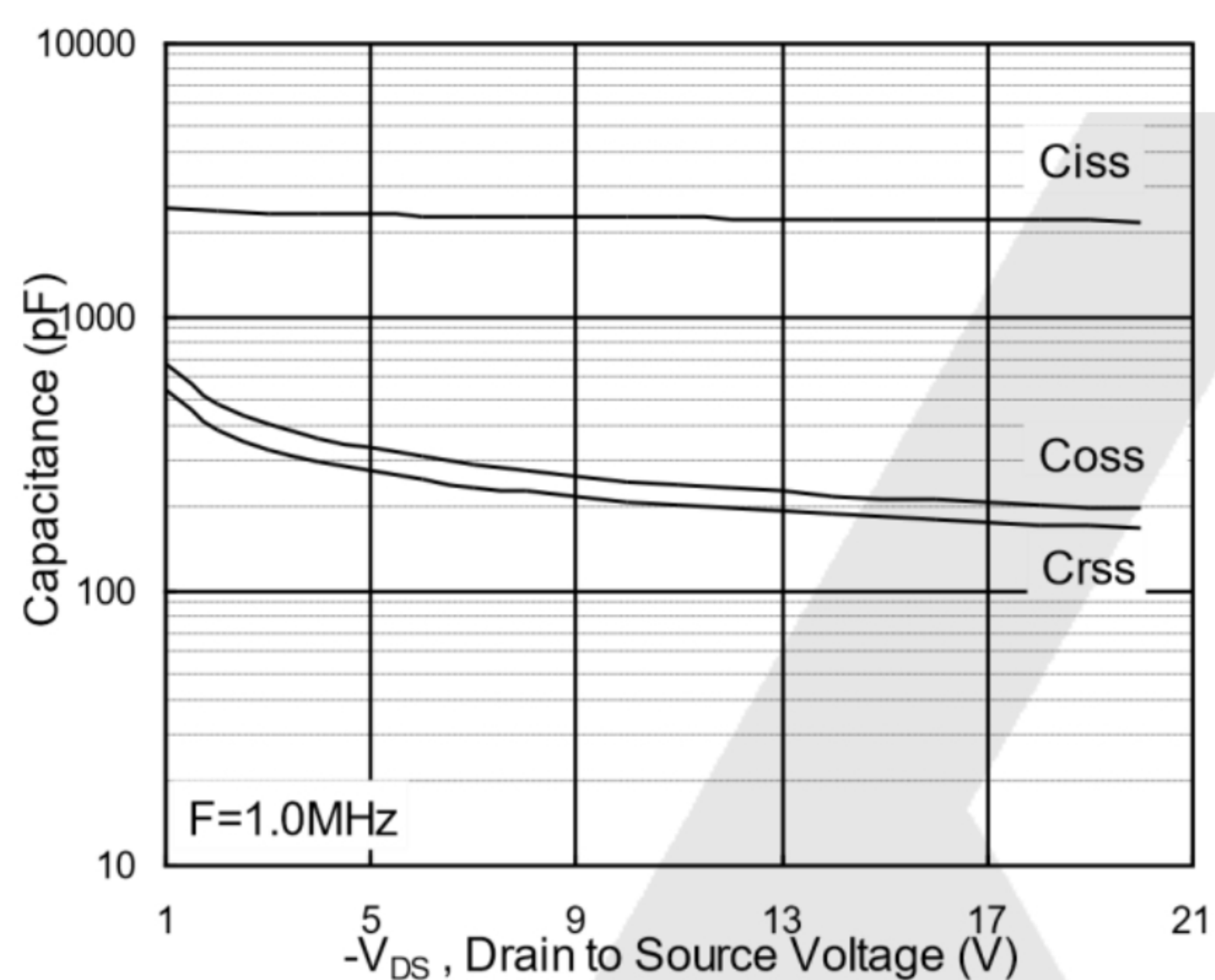


Fig.7 Capacitance

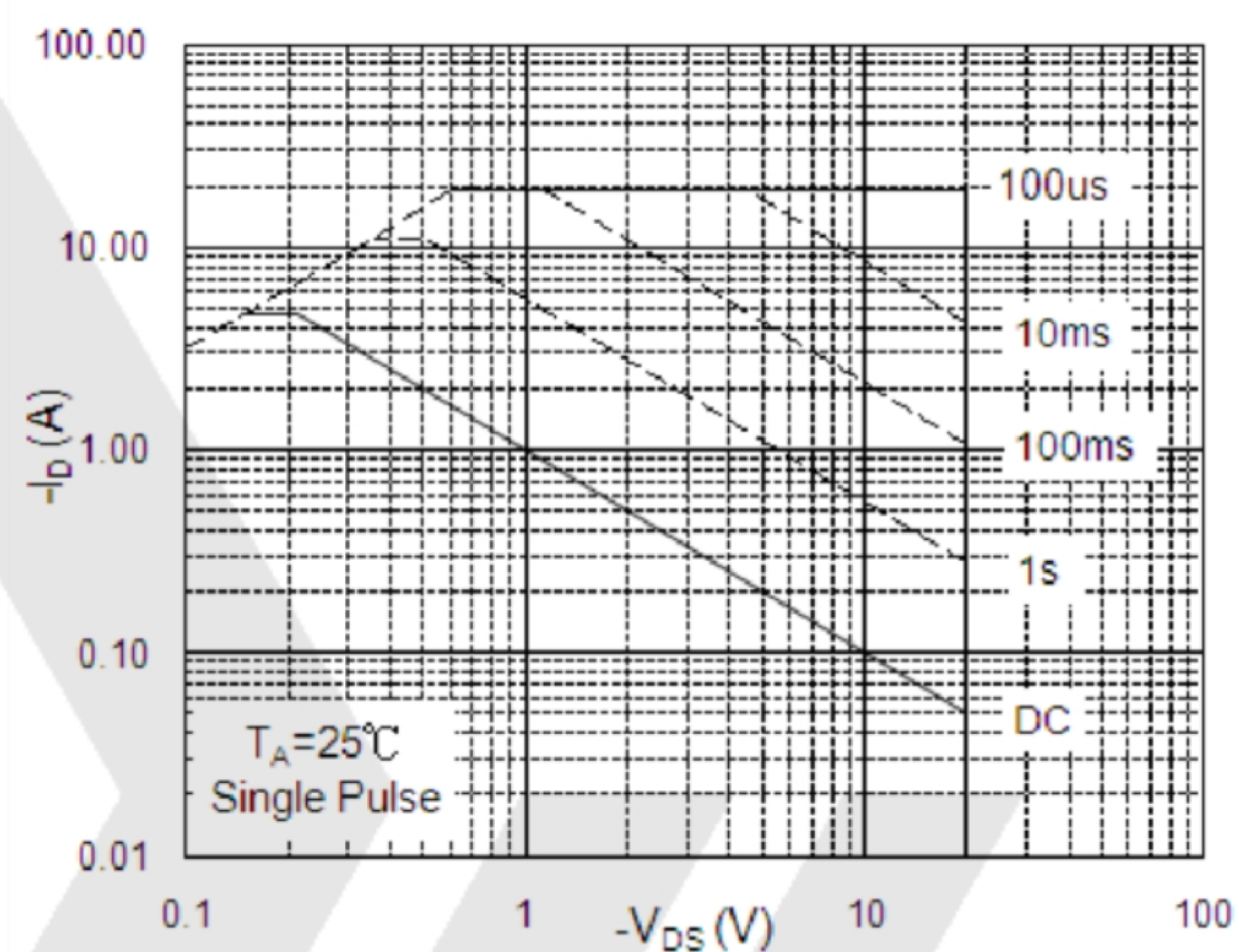


Fig.8 Safe Operating Area

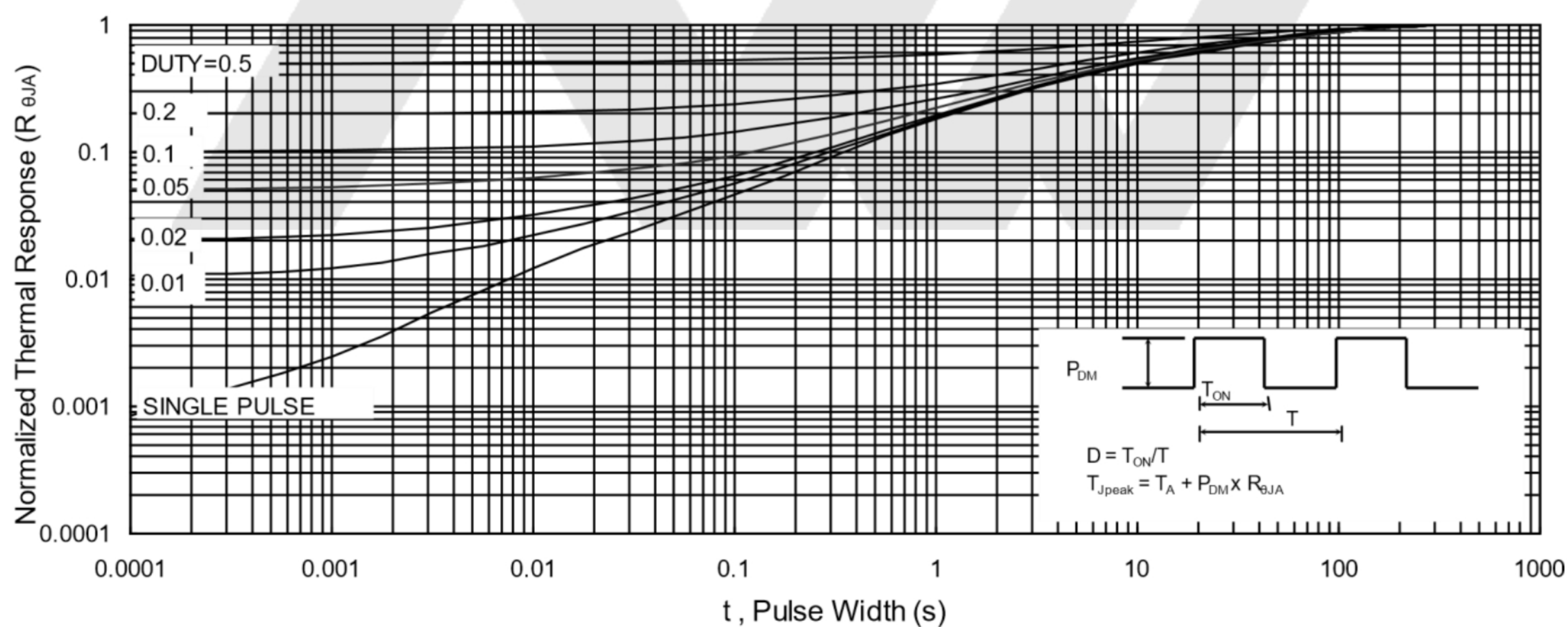


Fig.9 Normalized Maximum Transient Thermal Impedance

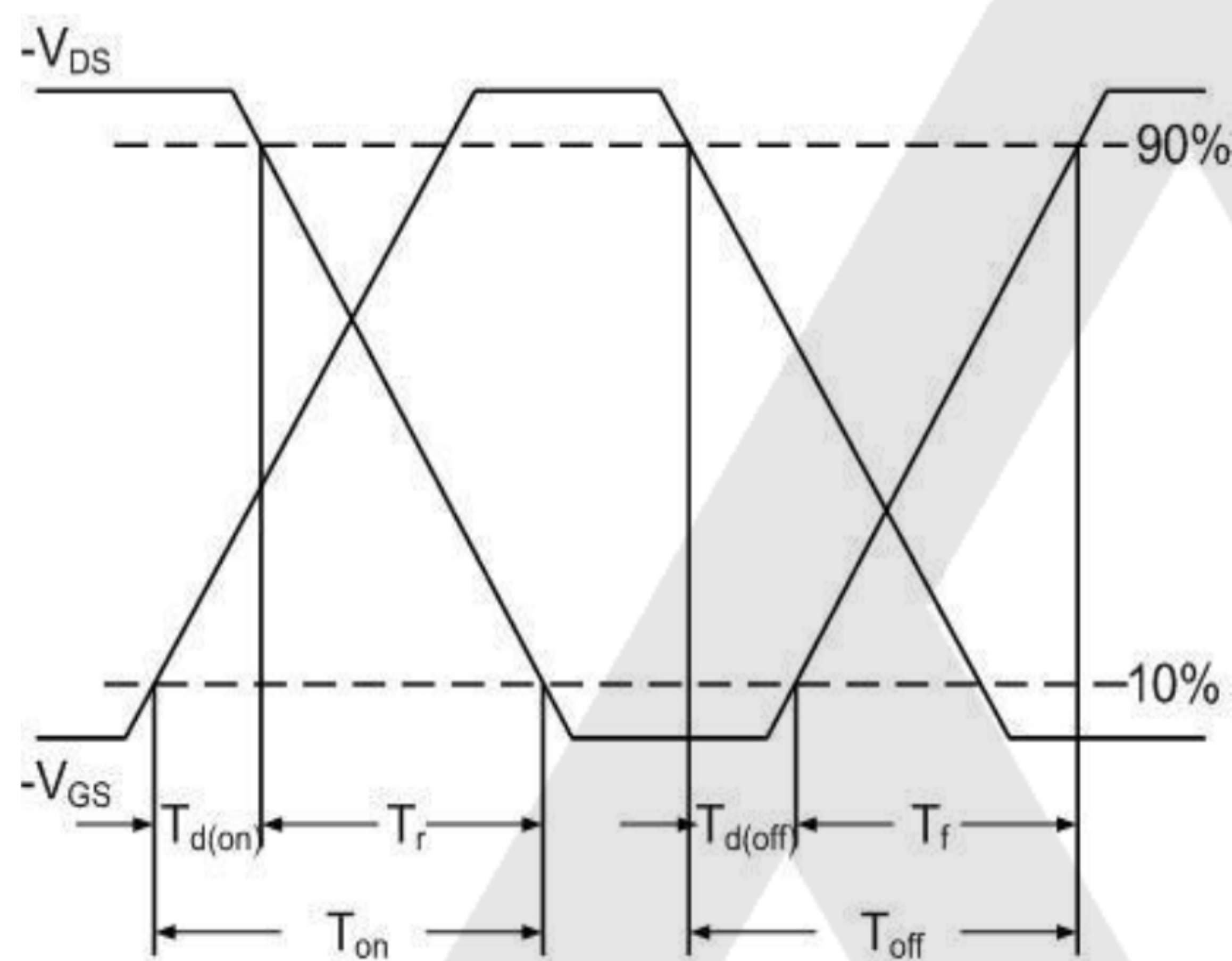


Fig.10 Switching Time Waveform

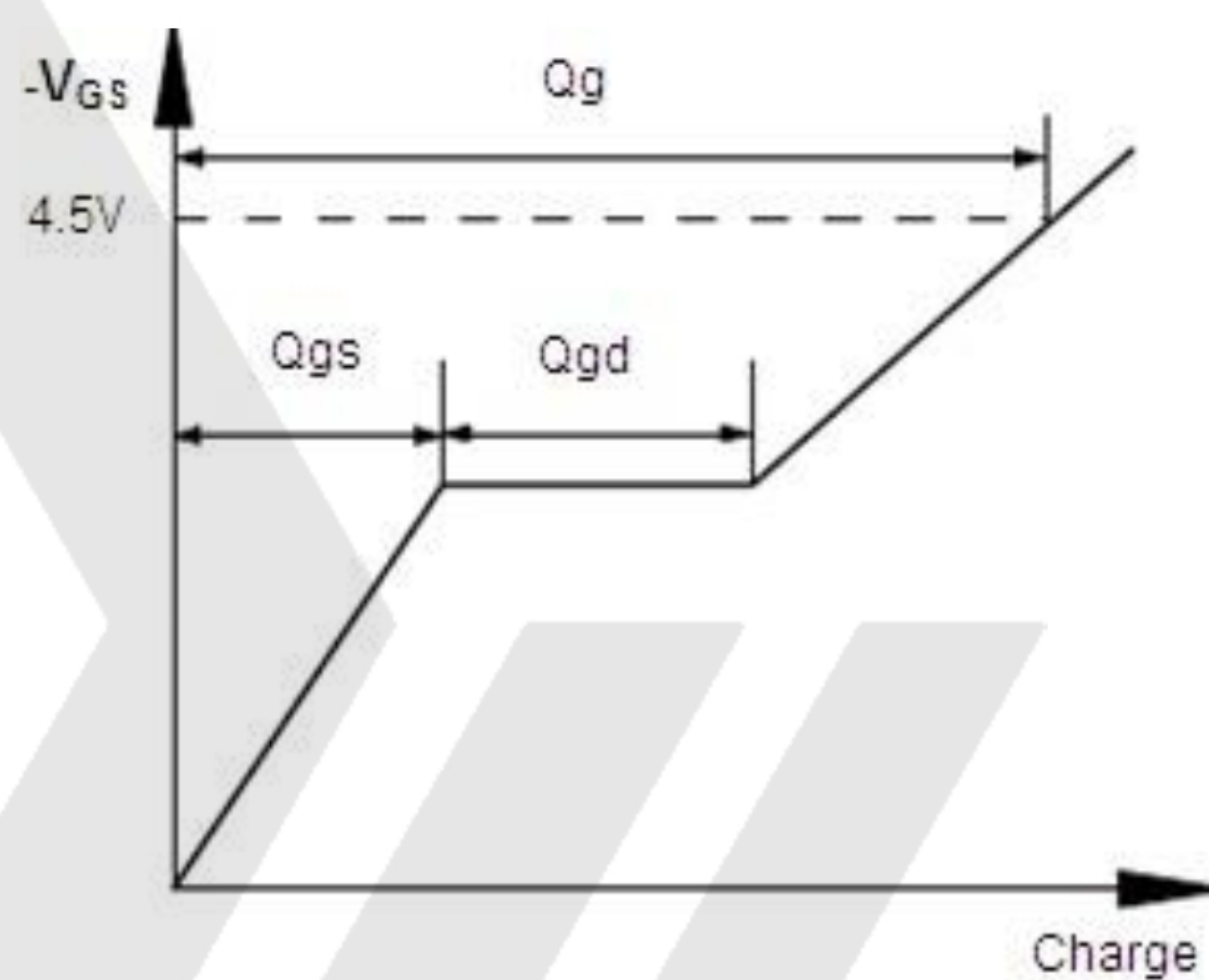
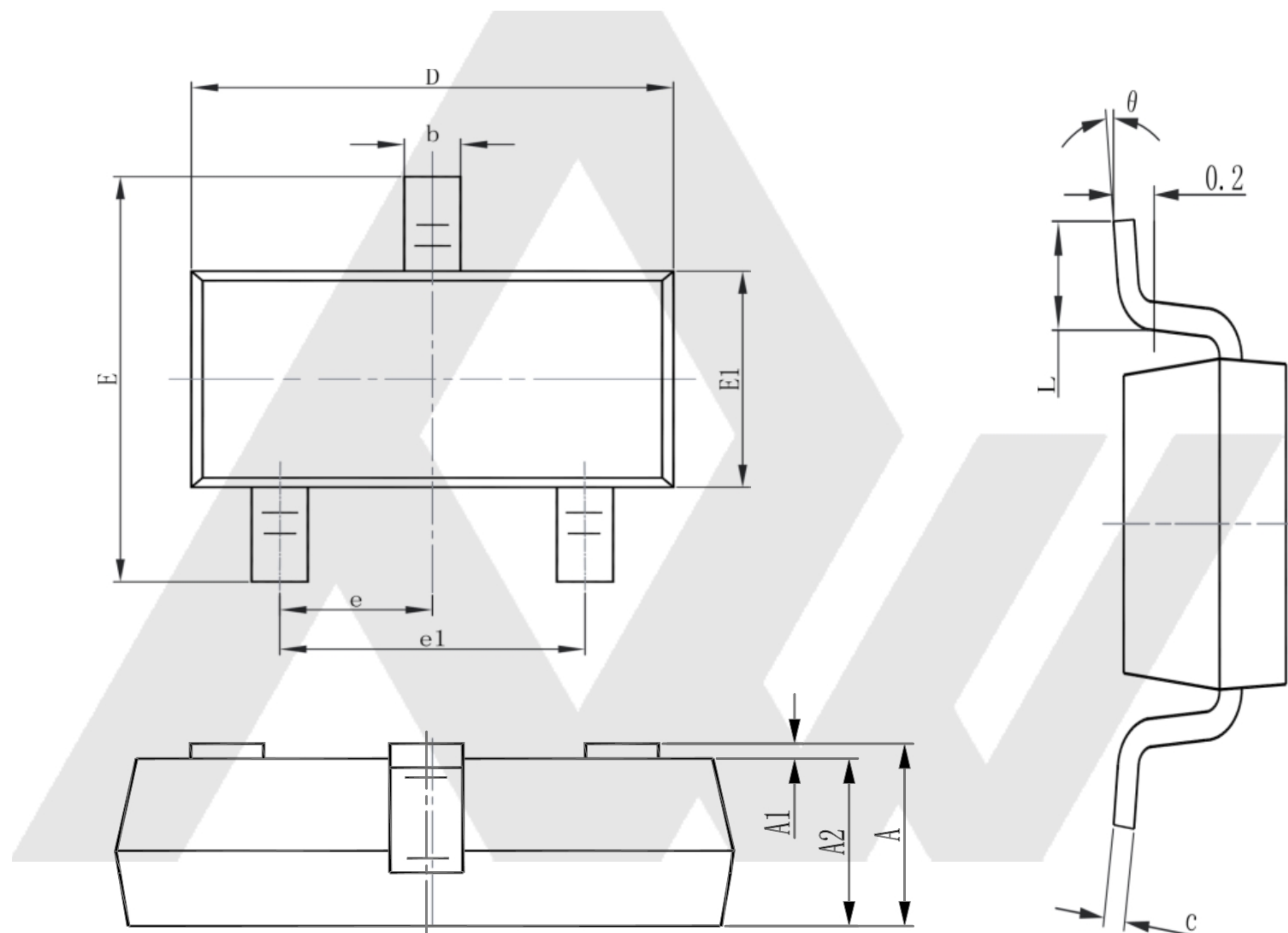


Fig.11 Gate Charge Waveform

PACKAGE DESCRIPTION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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