

## P-Channel Enhancement Mode MOSFET

### GENERAL DESCRIPTION

The PW2337 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### FEATURES

$V_{DS} = -100V$ ,  $I_D = -0.9A$

$R_{DS(ON)} < 650m\Omega$  @  $V_{GS} = -10V$

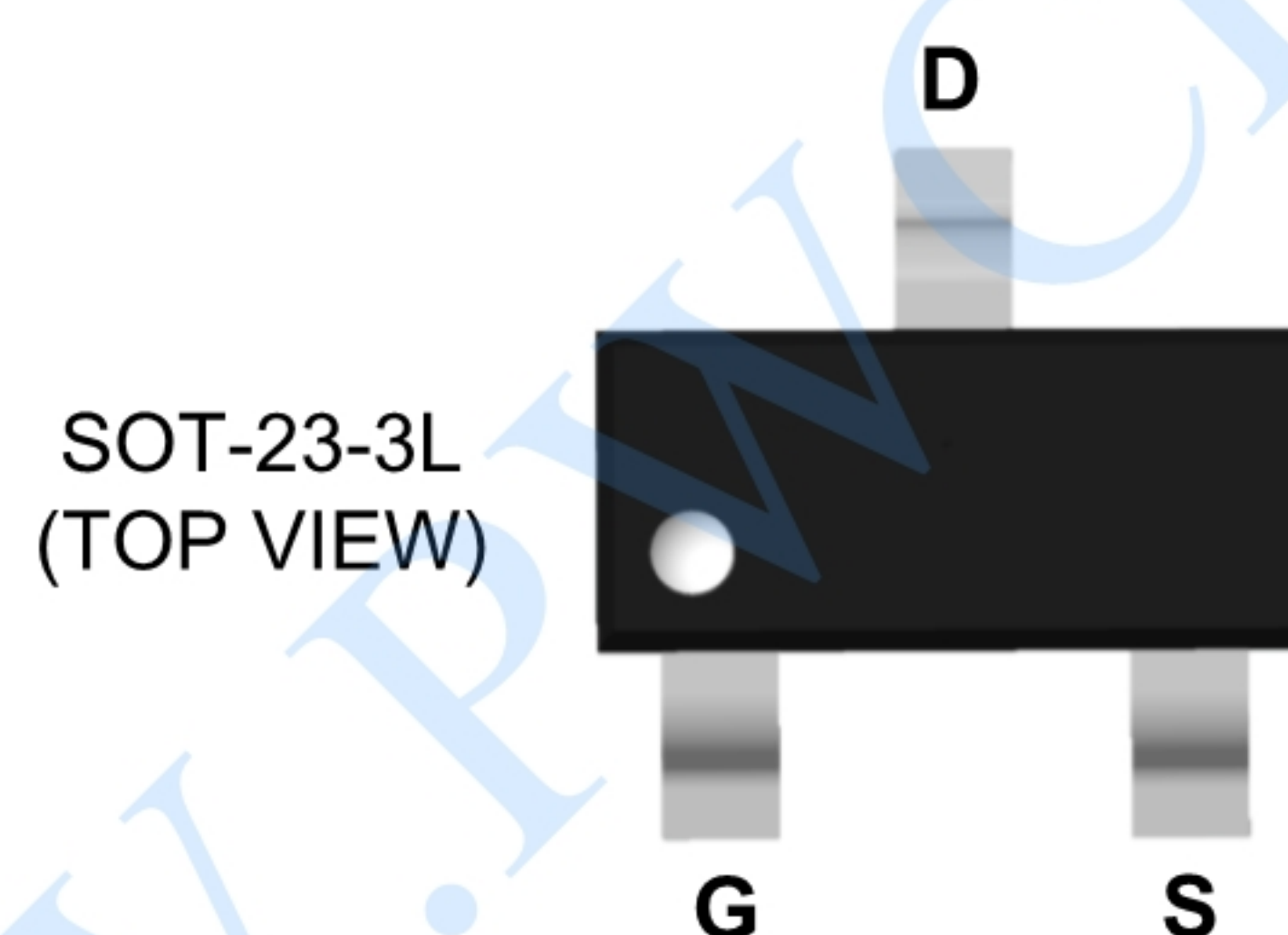
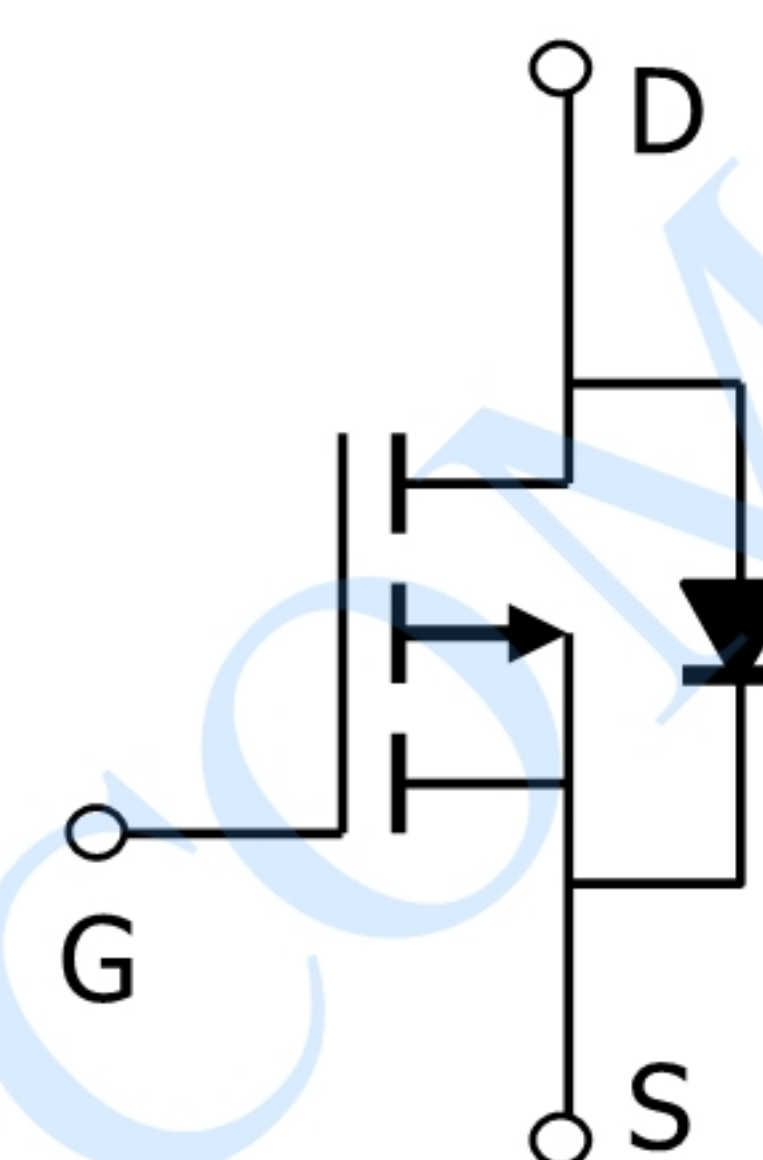
Available in a 3-Pin SOT23-3 Package

### Application

Battery protection

Load switch

Uninterruptible power supply



### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ -10V$ (NOTE1)	$I_D @ T_A = 25^\circ C$	-0.9	A
	$I_D @ T_A = 70^\circ C$	-0.7	A
Pulsed Drain Current (NOTE2)	$I_{DM}$	-1.8	A
Total Power Dissipation (NOTE3)	$P_D @ T_A = 25^\circ C$	1	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$
Thermal Resistance Junction-Ambient (NOTE1)	$R_{\theta JA}$	125	$^\circ C/W$
Thermal Resistance Junction-Case (NOTE1)	$R_{\theta JC}$	80	$^\circ C/W$

Note 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

Note 2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$

Note 3. The power dissipation is limited by 150 $^\circ C$  junction temperature





## ELECTRICAL CHARACTERISTICS

(TA = 25°C, unless otherwise noted.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-100			V
$\Delta BV_{DSS}/\Delta T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.063		V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance (NOTE2)	V <sub>GS</sub> =-10V , I <sub>D</sub> =-0.8A		520	650	mΩ
		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-0.4A		561	699	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.5	-2.5	V
$\Delta V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			4.5		mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-80V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-10	uA
		V <sub>DS</sub> =-80V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			-100	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-0.8A		3		S
Q <sub>g</sub>	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-15, V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-0.5		4.5		nC
Q <sub>gs</sub>	Gate-Source Charge			1.14		nC
Q <sub>gd</sub>	Gate-Drain Charge			1.5		nC
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V R <sub>G</sub> =3.3Ω, I <sub>D</sub> =-0.5A		13.6		ns
T <sub>r</sub>	Rise Time			6.8		ns
T <sub>d(off)</sub>	Turn-Off Delay Time			34		ns
T <sub>f</sub>	Fall Time			3		ns
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		553		pF
C <sub>oss</sub>	Output Capacitance			29		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			20		pF
I <sub>S</sub>	Continuous Source Current(NOTE1, 3)	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-0.9	A
I <sub>SM</sub>	Pulsed Source Current(NOTE2, 3)				-1.8	A
V <sub>SD</sub>	Diode Forward Voltage (NOTE2)	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V

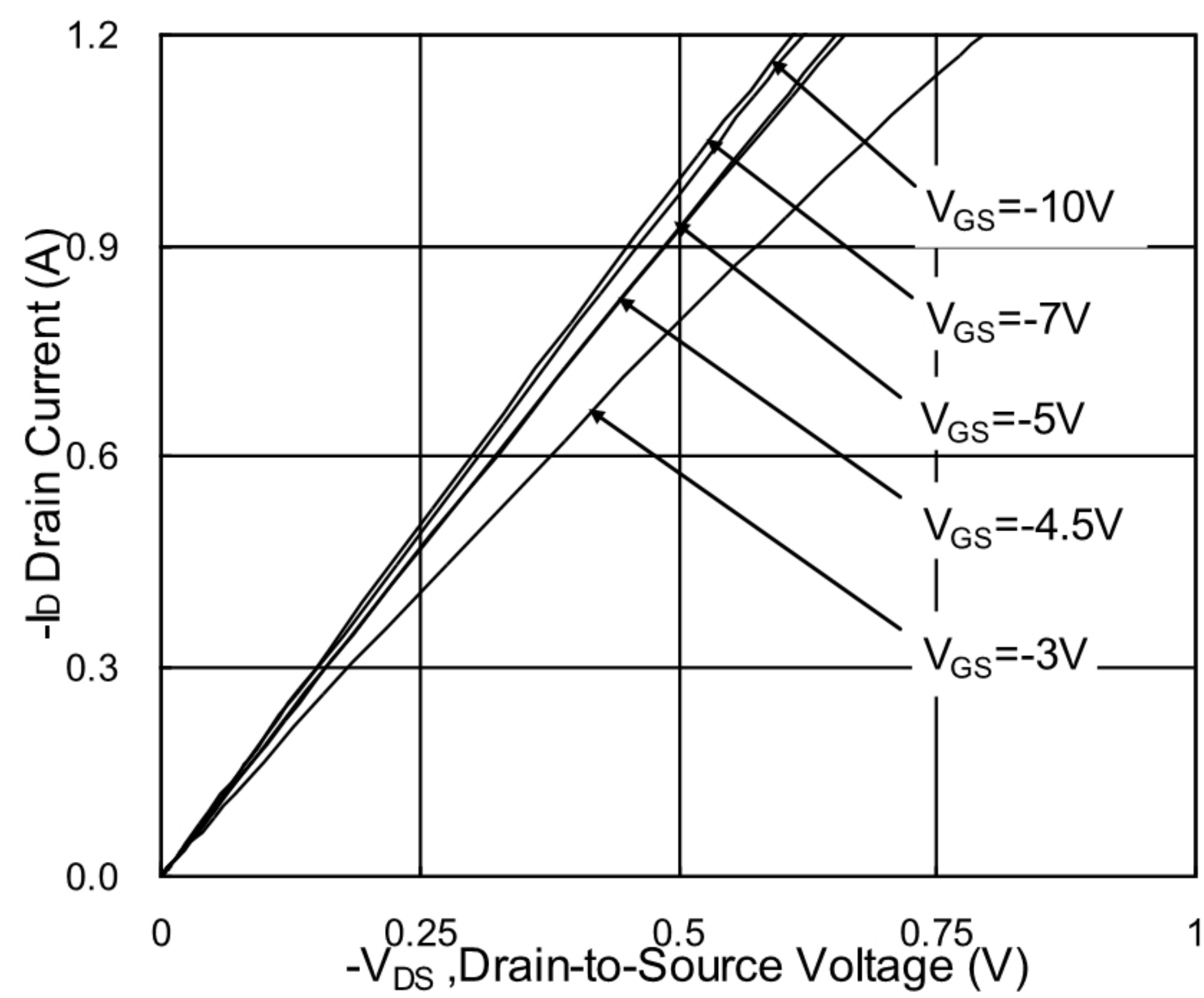
Note 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.

Note 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

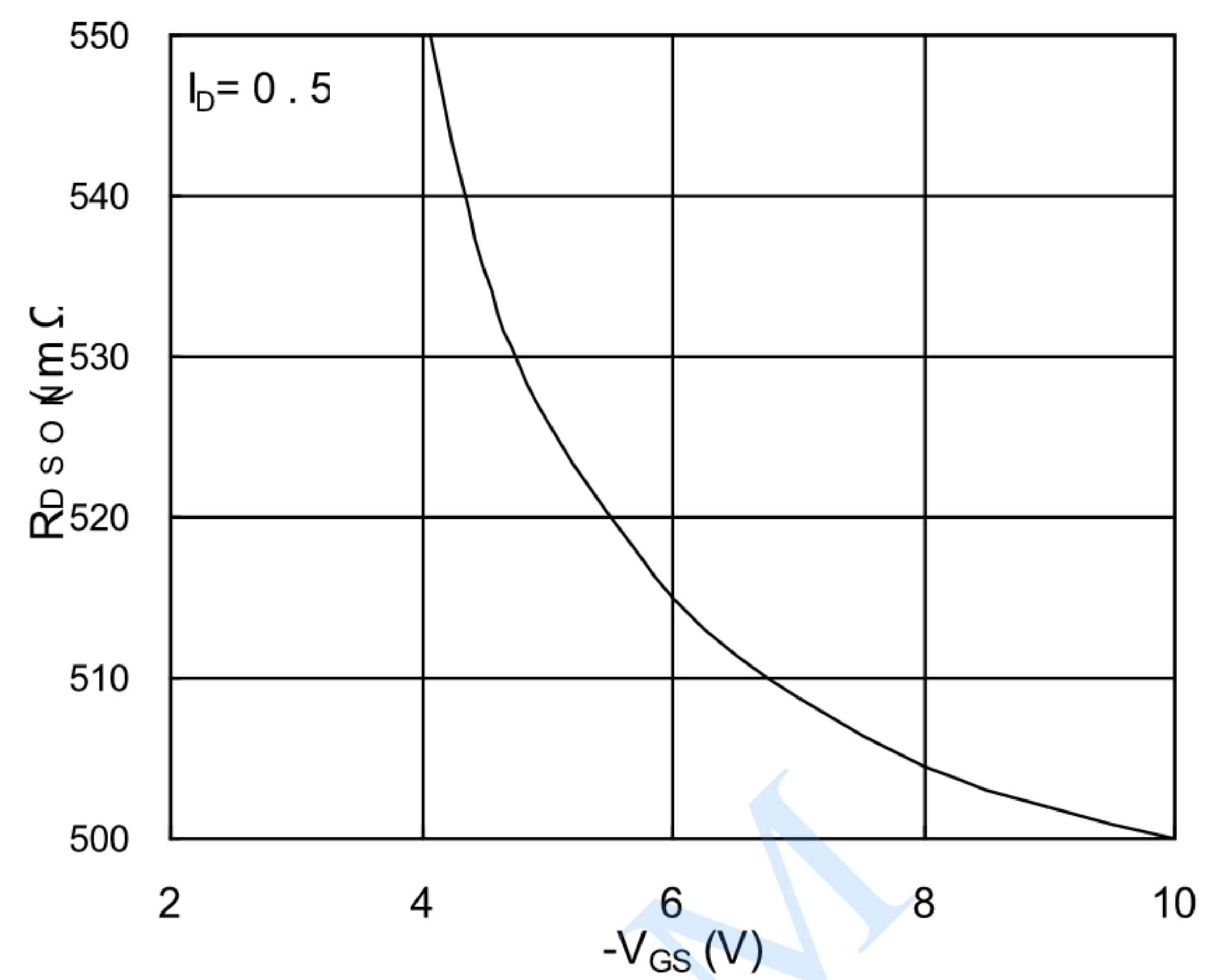
Note 3.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation



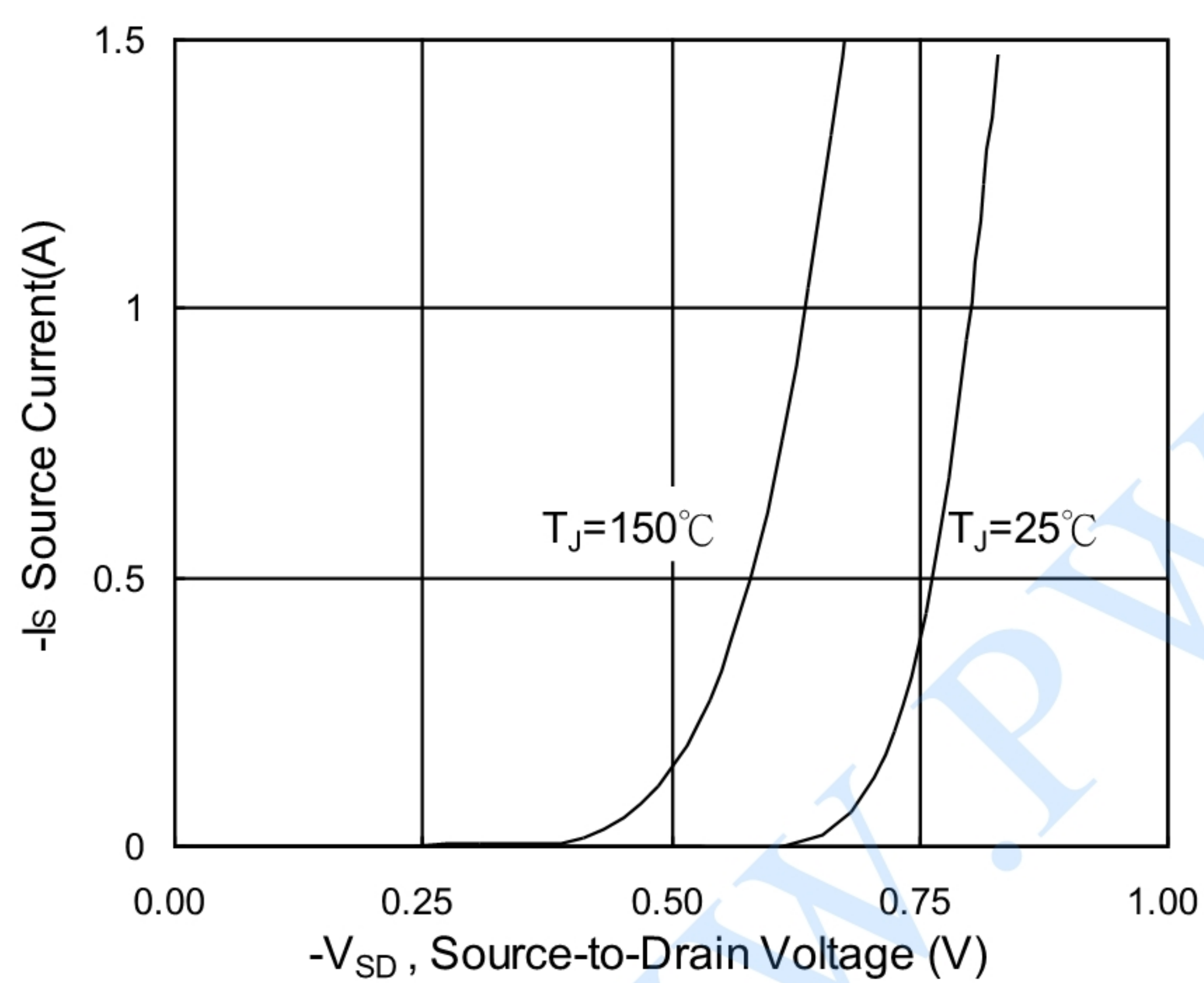
## Thermal Characteristics



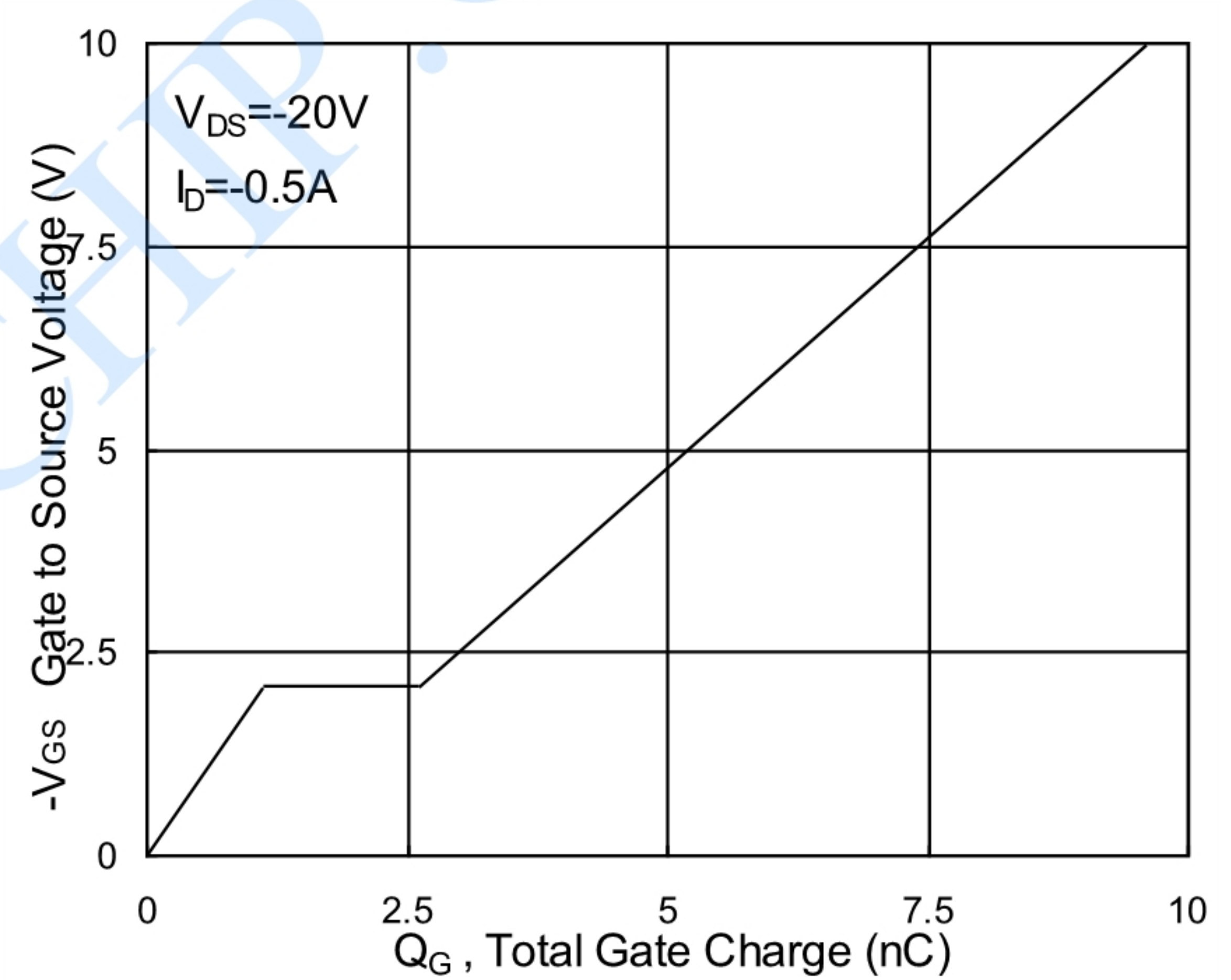
Typical Output Characteristics



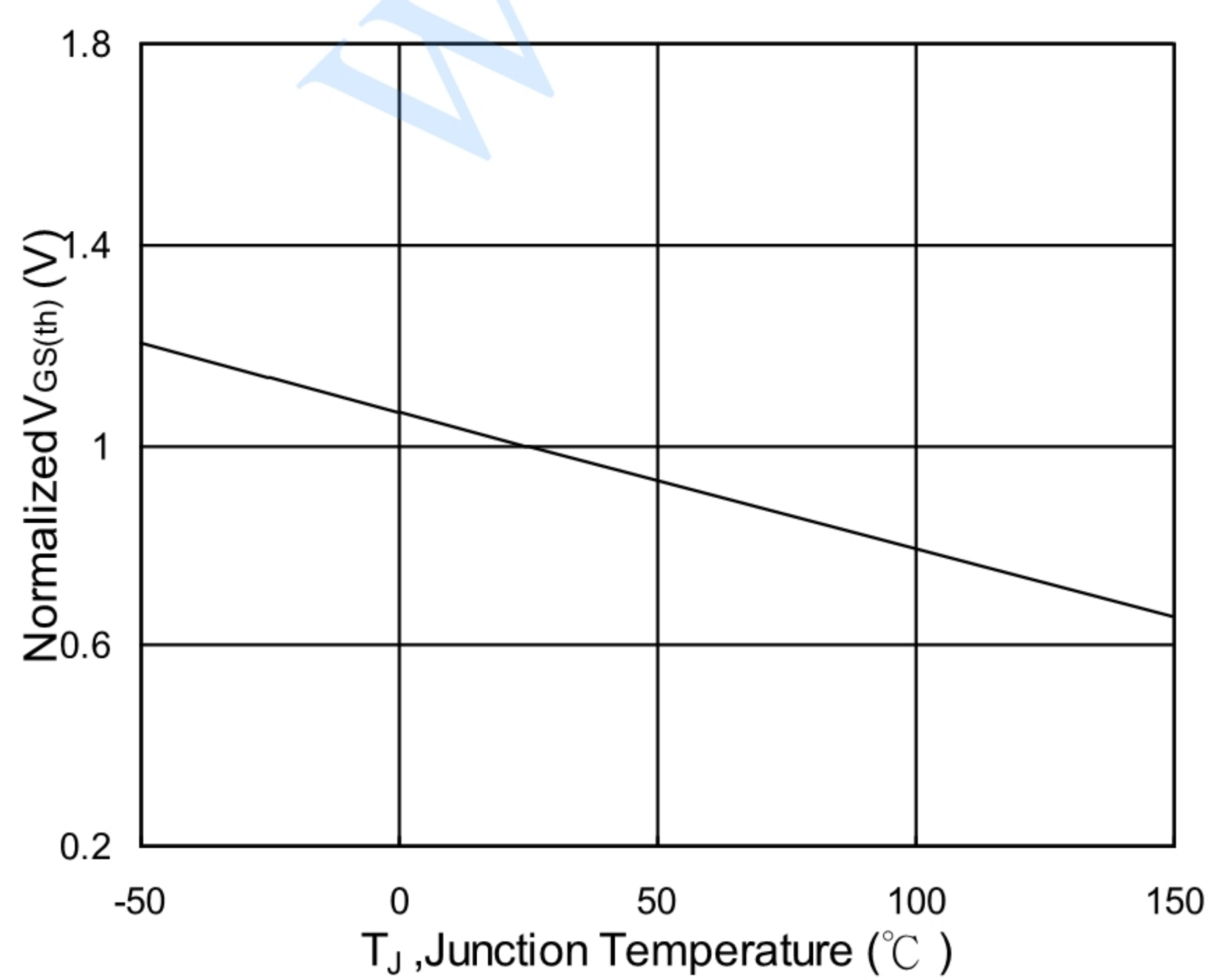
On-Resistance v.s Gate-Source



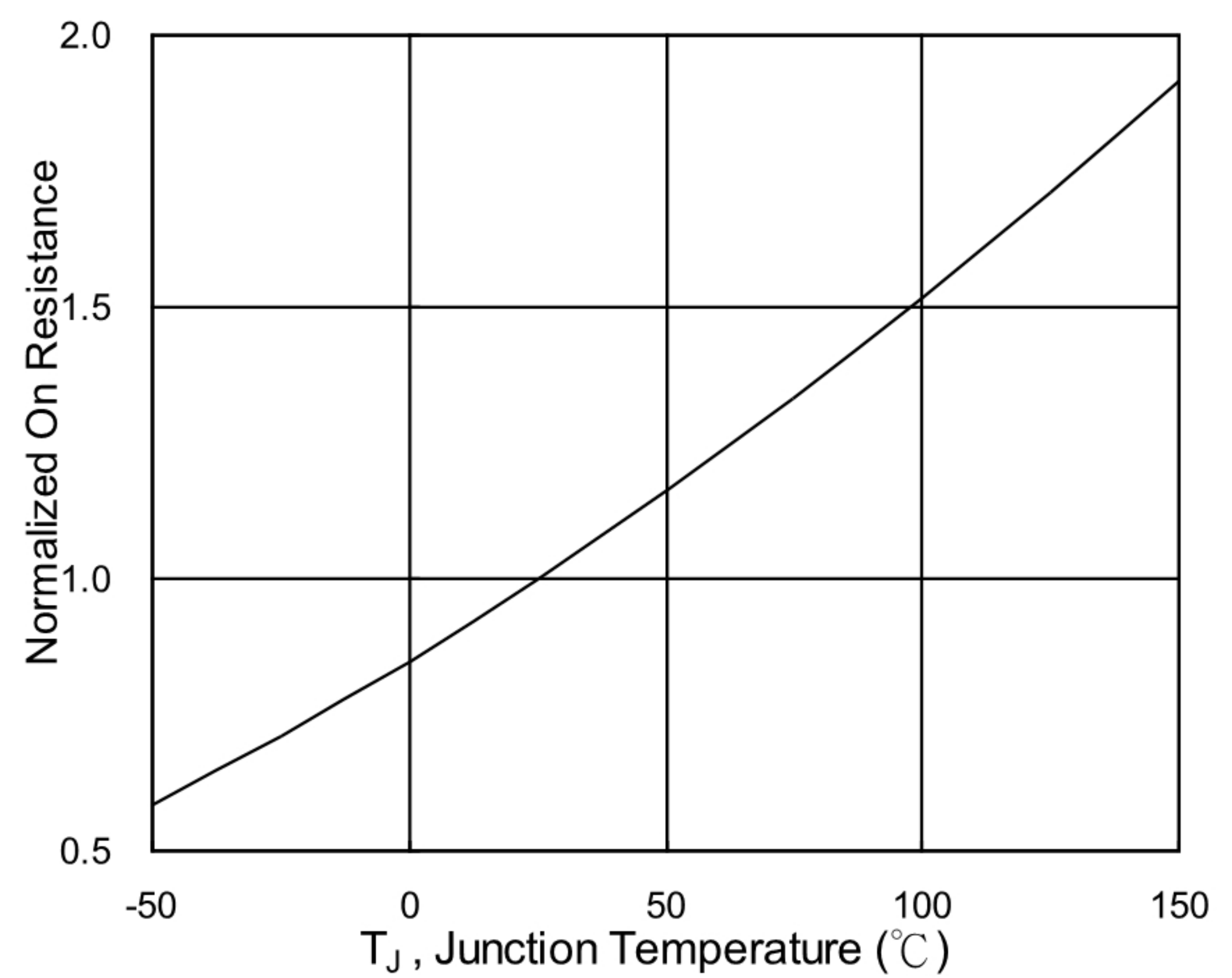
Forward Characteristics Of Reverse



Gate-Charge Characteristics

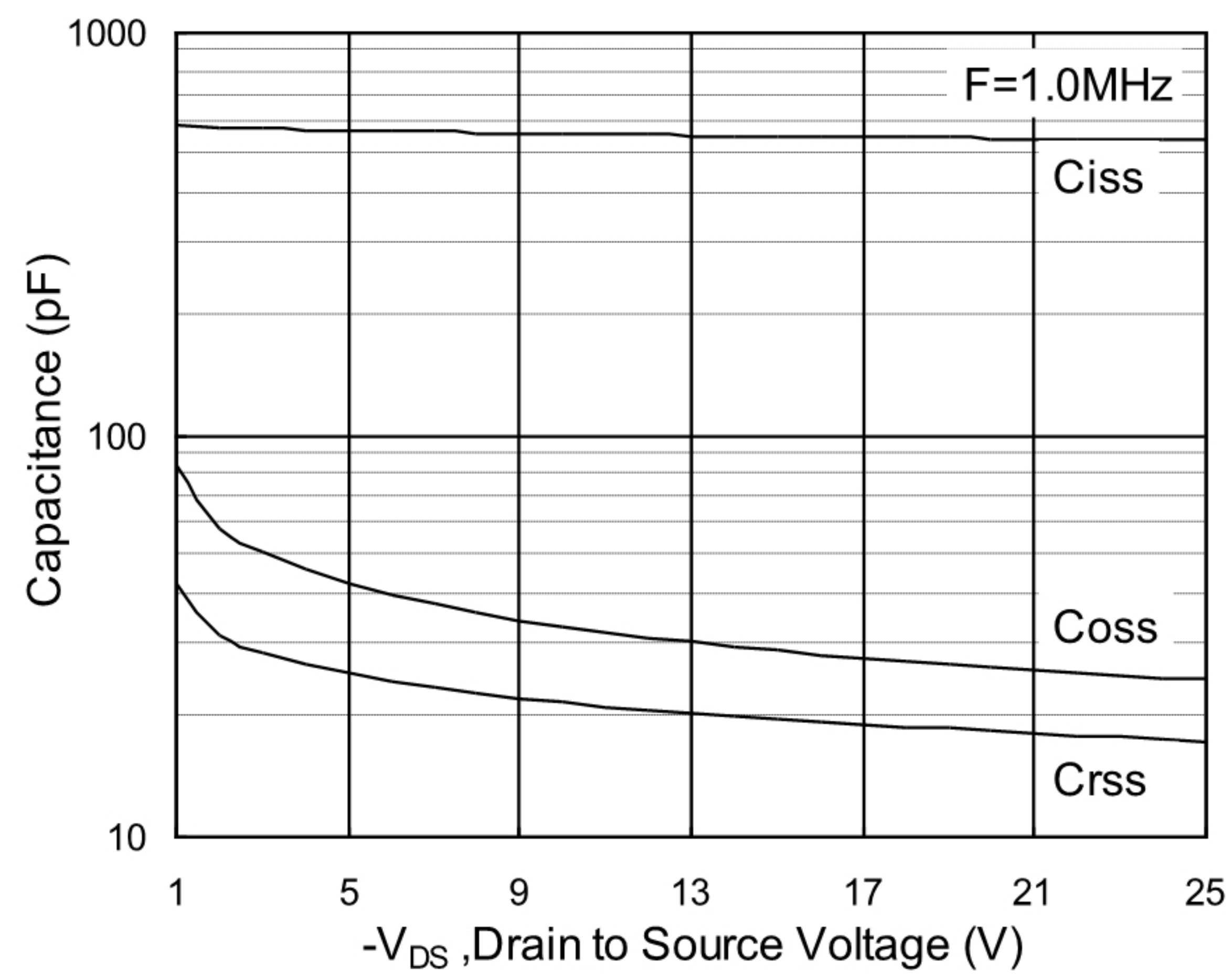


Normalized  $V_{GS(th)}$  v.s  $T_J$

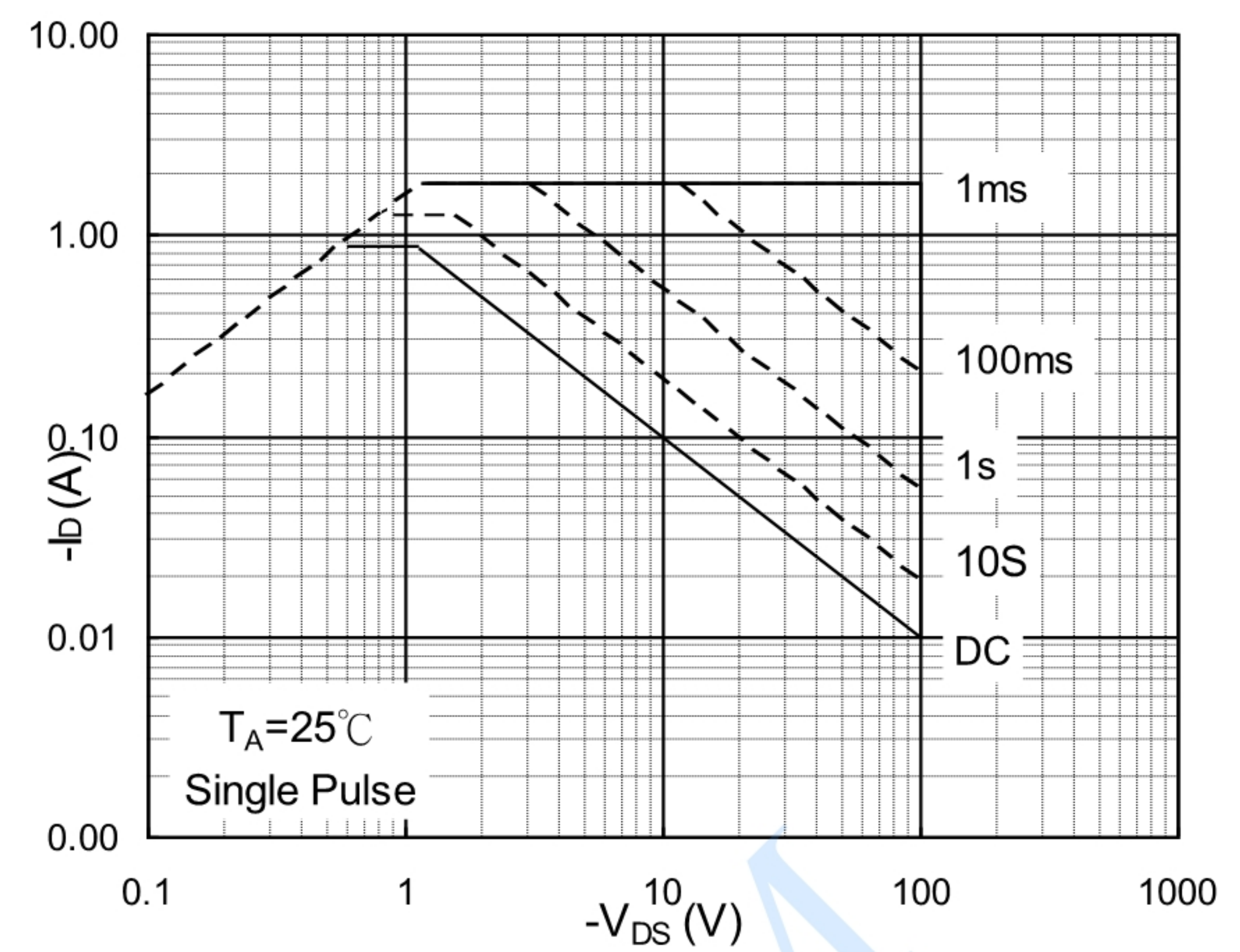


Normalized  $R_{DS(on)}$  v.s  $T_J$

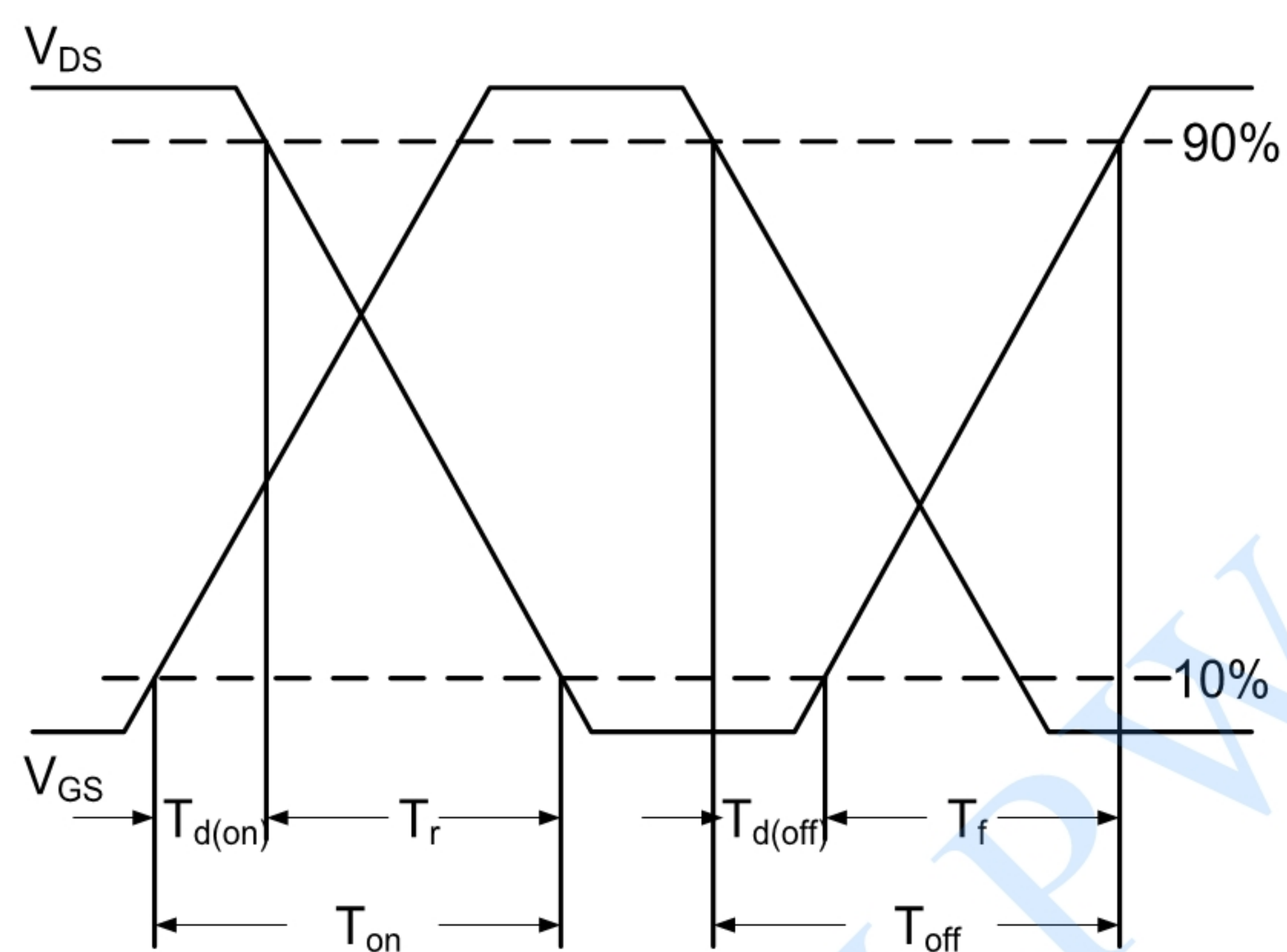




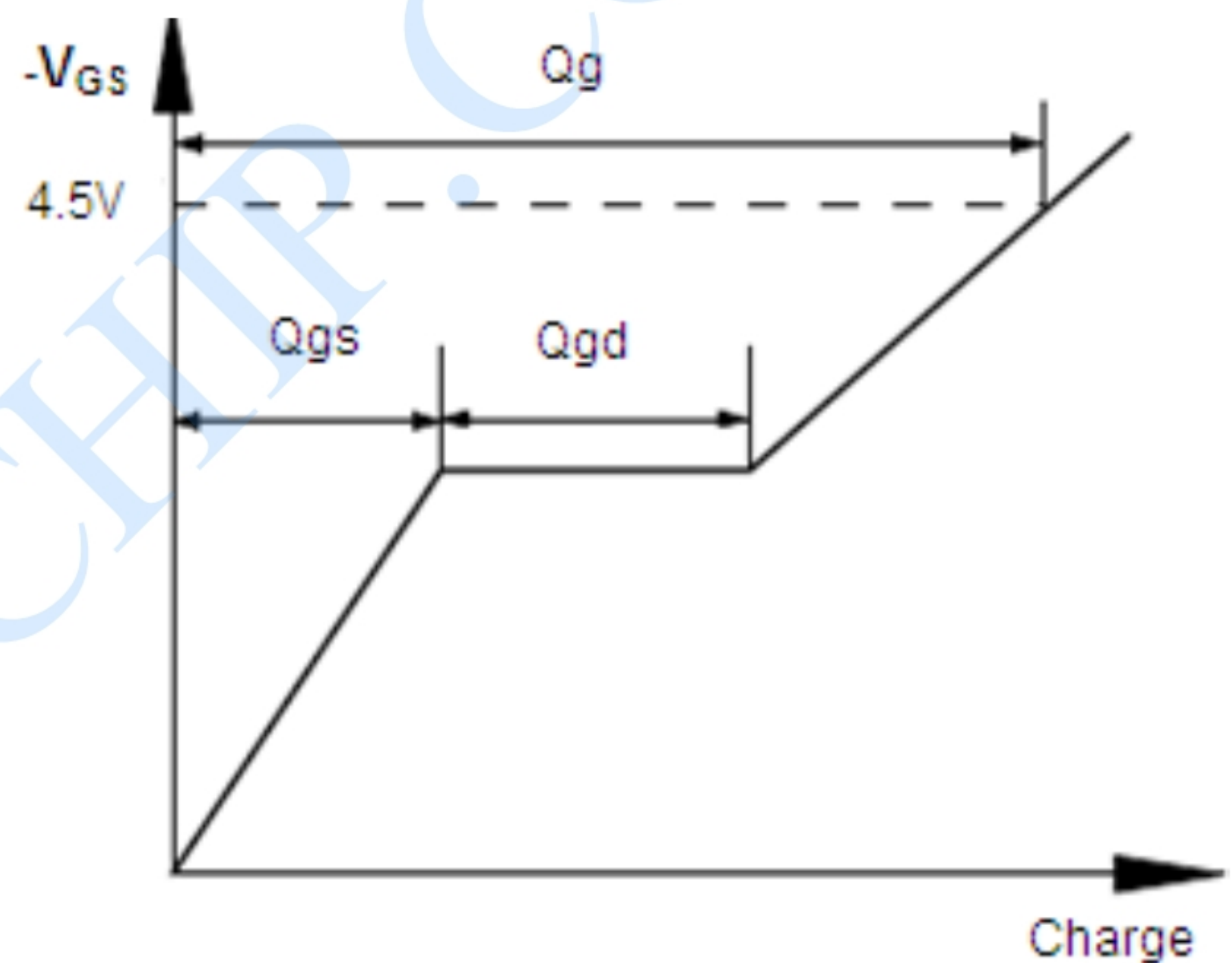
Capacitance



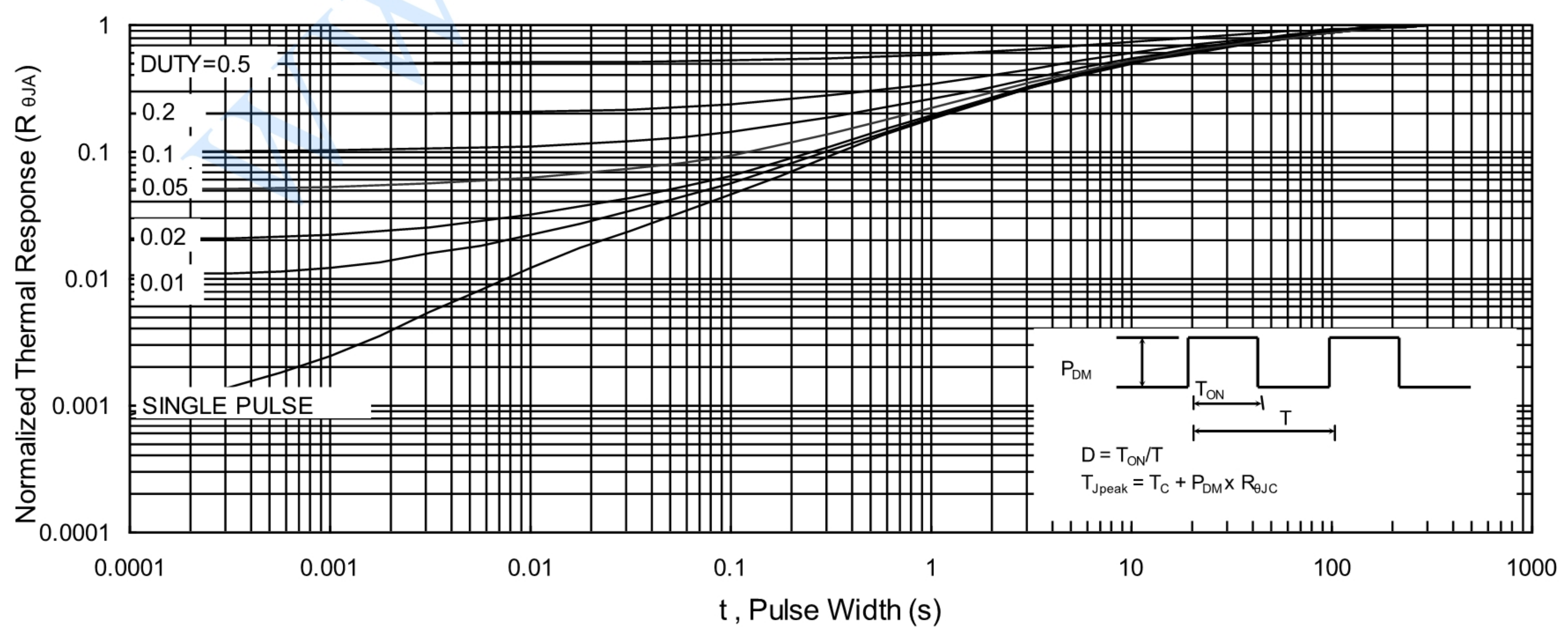
Safe Operating Area



Switching time waveform



Gate Charge waveform

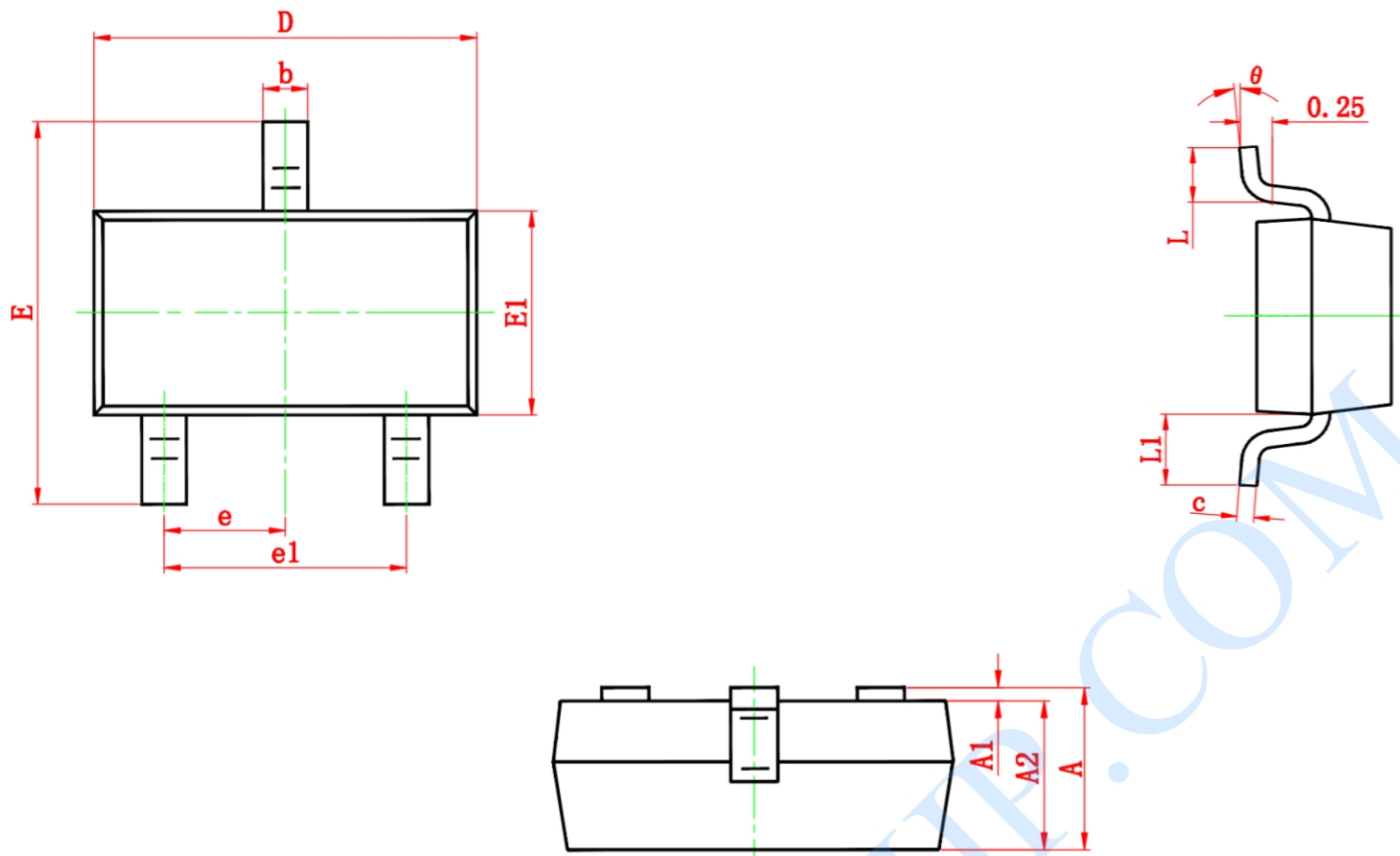


Normalized Maximum Transient Thermal Impedance



## PACKAGE DESCRIPTION

### SOT23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	2.250	2.550	0.089	0.100
E1	1.200	1.400	0.047	0.055
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.300	0.500	0.012	0.020
L1	0.550 REF.		0.022 REF.	
θ	0°	8°	0°	8°

### Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.





---

## IMPORTANT NOTICE

Wuxi PWChip Semi Technology CO., LTD (PW) reserves the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any products or services. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

PW assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using PW components.

PW products are not authorized for use in safety-critical applications (such as life support devices or systems) where a failure of the PW product would reasonably be expected to affect the safety or effectiveness of that devices or systems.

The information included herein is believed to be accurate and reliable. However, PW assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.